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VEHICLE EQUIPMENT CONTROL WITH
SEMICONDUCTOR LIGHT SENSORS

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This is a divisional of U.S. Patent Application No. 09/491,192 filed on January 25, 2000, entitled "VEHICLE EQUIPMENT CONTROL WITH SEMICONDUCTOR LIGHT SENSORS," which is a continuation-in-part of U.S. Patent Application No. 09/307,941, entitled "AUTOMATIC DIMMING MIRROR USING SEMICONDUCTOR LIGHT SENSOR WITH INTEGRAL CHARGE COLLECTION," filed on May 7, 1999, which is a continuation-in-part of U.S. Patent Application No. 09/236,969, entitled "AUTOMATIC DIMMING MIRROR USING SEMICONDUCTOR LIGHT SENSOR WITH INTEGRAL CHARGE COLLECTION," filed on January 25, 1999, now abandoned, the entire disclosures of which are incorporated herein by reference thereto.

[0002] This application is also a continuation-in-part of U.S. Patent Application No. 09/307,191, entitled "PHOTODIODE LIGHT SENSOR," filed on May 7, 1999, and a continuation-in-part of U.S. Patent Application No. 09/290,966, entitled "MOISTURE DETECTING SYSTEM USING SEMICONDUCTOR LIGHT SENSOR WITH INTEGRAL CHARGE COLLECTOR," filed on April 13, 1999, now U.S. Patent No. 6,313,457 both of which are continuation-in-parts of U.S. Patent Application No. 09/237,107, entitled "PHOTODIODE LIGHT SENSOR," filed on January 25, 1999, now abandoned, the entire disclosures of all of which are incorporated herein by reference thereto.

BACKGROUND OF THE INVENTION

[0003] The present invention relates to automatically controlled automotive vehicle equipment of the type using light sensors to monitor light levels.

BACKGROUND ART

[0004] The continuing reduction in the size and cost of electronic circuits, in particular microprocessors, makes possible the inclusion of an increasing amount of intelligence for the automatic control of automotive vehicle equipment. Examples include: rearview mirrors that adjust their reflectivity in response to the levels of ambient light and glare from other vehicles; moisture on windows sensed and removed by automatic wipers, defrosters, defoggers, and the like; windows that automatically close when rain is detected; headlamps switched in response to ambient light levels; and heating and cooling of the vehicle passenger compartment automatically adjusted in anticipation of changes in external conditions.

[0005] Systems that automatically control automotive equipment can advantageously employ one or more sensors for measuring light levels. Automatically dimmable rearview mirrors, and in particular electrochromic mirrors, using light sensors, are described in U.S. Patent Nos. 4,902,108 to Byker; 5,724,187 to Varaprasad et al.; 5,928,572 to Tonar et al.; and U.S. Patent No. 6,020,987 to Baumann et al. In the case of mirrors having automatic reflectivity control, such as electrochromic mirrors, it is advantageous to use sensors to detect both forward and rear light levels. Among the dual sensor designs proposed include those described in U.S. Patent Nos. 3,601,614 to Platzer; 3,746,430 to Brean et al.; 4,580,875 to Bechtel et al.; 4,793,690 to Gahan et al.; 4,886,960 to Molyneux et al.; 4,917,477 to Bechtel et al.; 5,204,778 to Bechtel; 5,451,822 to Bechtel et al.; and 5,715,093 to Schierbeek et al. A vision system is disclosed in U.S. Patent Application Serial No. 09/001,855, entitled "VEHICLE

VISION SYSTEM," filed by Jon H. Bechtel et al. on December 31, 1997, the disclosure of which is incorporated herein by reference thereto.

[0006] Various moisture detectors are also known that employ a light sensor. Examples of such detectors include those described in U.S. Patent Nos. 5,821,863 to Schröder et al.; 5,796,106 to Noack; 5,661,303 to Teder; 5,386,111 to Zimmerman; 4,973,844 to O'Farrell et al.; 4,960,996 to Hochstein; 4,930,742 to Schofield et al.; 4,871,917 to O'Farrell et al.; 4,867,561 to Fujii et al.; 4,798,956 to Hochstein; 4,652,745 to Zanardelli; and RE 35,762 to Zimmerman. A moisture detection system is disclosed in U.S. Patent No. 5,923,027, entitled "MOISTURE SENSOR AND WINDSHIELD FOG DETECTOR USING AN IMAGE SENSOR," issued on July 13, 1999, to Joseph S. Stam et al., the disclosure of which is incorporated herein by reference thereto.

[0007] A variety of systems for controlling headlamps using a light sensor are also known, including those described in U.S. Patent Nos. 4,891,559 to Matsumoto et al.; 5,036,437 to Macks; 5,235,178 to Hegyi; 5,537,003 to Bechtel et al.; 5,416,318 to Hegyi; 5,426,294 to Kobayashi et al.; 5,666,028 to Bechtel et al., and 5,942,853 to Piscart. Such systems employ a light sensor to detect conditions under which the headlamp light intensity is altered. Other systems are disclosed in U.S. Patent No 5,837,994, entitled "CONTROL SYSTEM TO AUTOMATICALLY DIM VEHICLE HEAD LAMPS," issued November 17, 1998, to Joseph S. Stam et al.; U. S. Patent No. 5,990,469, entitled "CONTROL CIRCUIT FOR IMAGE ARRAY SENSORS," issued to Jon H. Bechtel et al. on November 23, 1999, and U.S. Patent No. 5,998,929, entitled "CONTROL SYSTEM FOR AUTOMOTIVE VEHICLE HEADLAMPS AND OTHER VEHICLE EQUIPMENT," issued on December 7, 1999, to Jon H. Bechtel et al., the disclosures of which are incorporated herein by reference thereto.

[0008] Such automatically controlled equipment may employ one or more cadmium sulfide (CdS) cell as a light sensor. CdS cells are photosensitive resistors exhibiting increasing conductance with increasing light levels. CdS cells offer some advantages, such as being relatively low in cost, demonstrating good sensitivity to low light levels, and providing a spectral response somewhat similar to that of the human eye. However, equipment employing such cells can not fully realize these advantages due to other characteristics of CdS cells, such as: a high degree of variance between cells, slow response at low light levels, poor environmental stability, limited dynamic range, and difficulty being assembled in automated electronic manufacturing processes and equipment. Rearview mirrors employing CdS cells for sensing ambient light and glare may incorporate the CdS cell into a full or partial bridge to increase the dynamic range of the cell. However, the bridge output will only represent a fixed relationship between an ambient light level and a glare level, which fixed relationship is often not appropriate throughout the range of ambient light levels monitored.

[0009] Vehicle equipment, such as automatic dimming mirrors, has also used one or more discrete photodiodes configured as a light-dependent current source. Relative to equipment using CdS cells, equipment using photodiodes will experience less operational variance due to the light sensor part performance, will demonstrate better environmental stability, and will be more easily adapted to automated manufacturing. However, photodiodes themselves are relatively expensive and produce very low currents at low light levels. These low currents require the inclusion of special amplification techniques to achieve a useful signal for the electronic components, increasing the cost and complexity of the equipment.

[0010] Another approach to providing equipment responsive to ambient light is described in U.S. Patent No. 5,760,962 issued to Schofield et al. wherein an automatically dimmable

mirror is disclosed that incorporates a large imaging array to gather light from behind and beside the vehicle. Each light transducer, or pixel, within the array views a separate area within the target spatial distribution of the light sensor. The equipment measures ambient light by examining pixels generally directed sideways. The cost of the imaging array, the required lens, and the complicated signal processing logic make equipment using the imaging array prohibitively expensive for many automotive applications. An additional problem is that light collected from a side view less accurately represents the ambient light experienced by the vehicle operator than does light from a forward view.

[0011] One difficulty with providing equipment employing light sensors is the occurrence of operating anomalies when the equipment is subject to high temperatures. Some equipment employs light sensors that are extremely non-linear at high temperatures. Other equipment may suffer a permanent change in operating characteristics after being exposed to high temperatures. Such a permanent change can occur in equipment using a CdS cell exposed to prolonged sun on a hot day, such as prolonged exposure to temperatures in excess of 87°C. Sensors may even provide completely false readings, such as by identifying a bright light condition in low light conditions, due to excessive thermal noise. Traditionally, the only way to deal with this problem has been to incorporate a temperature sensor and additional electronics into the vehicle equipment to compensate for sensor performance changes resulting from temperature variations. Such electronics add cost and complexity to the equipment.

[0012] It can thus be seen that a difficulty with implementing automatically controlled equipment is accommodating the light sensor. Inclusion of light sensors typically introduces complex and costly manufacturing processes. However, the equipment needs to be inexpensive to fall within the range deemed acceptable by an automobile purchaser.

Additionally, manufacturers of vehicles incorporating such equipment must either accept inconsistent operating performance or use complex and costly circuitry and processes to accommodate these variations. Such additional provisions may be required to enable the equipment to operate with sufficiently consistent sensitivity across a wide dynamic range as is required for operation in the ranges of temperature, humidity, shock, and vibration experienced within a vehicle.

[0013] What is needed is more cost-effective equipment using light sensors operable over a wide range of light conditions and temperatures.

SUMMARY OF THE INVENTION

[0014] Automotive vehicle equipment is controlled by a system including at least one semiconductor light sensor having variable sensitivity to light. A light sensor generates a light signal indicative of the intensity of light incident on the light sensor. Control logic varies the sensitivity of light sensors and generates equipment control signals based on received light signals. Sensitivity of light sensors may be varied by changing the integration time of charge produced by light incident on light transducers, by selecting between light transducers of different sensitivity within the light sensor, by using a light transducer with a sensitivity that is a function of the amount of incident light, and the like.

[0015] In one embodiment, the system for automatically controlling vehicle equipment includes at least one semiconductor light sensor outputting a discrete light signal based on light incident over a variable integration period. Control logic generates at least one equipment control signal based on the discrete light signal.

[0016] In another embodiment, the vehicle equipment includes a rearview mirror having a dimming element with a variably reflective surface, the degree of reflectivity based on the

equipment control signal. The light sensors include at least one of an ambient light sensor positioned to receive light generally in front of the vehicle and a glare sensor positioned to view a scene generally behind a vehicle operator.

[0017] In still another embodiment, the vehicle equipment includes at least one headlamp. The light sensors include at least one ambient light sensor positioned to receive light generally in front of and above the vehicle. The light sensors may be a first ambient light sensor admitting light in a first band of frequencies and a second ambient light sensor admitting light in a second band of frequencies different from the first band of frequencies. The control logic can determine a first filtered ambient light level from the light signal output from the first ambient light sensor and a second filtered ambient light level from the light signal output from the second ambient light sensor. A threshold based on the first filtered ambient light level and the second filtered ambient light level is found. A headlamp control signal based on the threshold and at least one of the first filtered ambient light levels and the second ambient light level is generated.

[0018] In yet another embodiment, the control of vehicle equipment is based on detecting the presence of moisture on a window. The system includes an emitter for emitting light at the window. At least one light sensor is positioned to receive light from the emitter reflected from the window. The control logic receives a first light signal from the light sensor with the emitter turned off. The emitter is turned on and a second light signal is received from the light sensor. The presence of moisture is determined based on the first light signal and the second light signal.

[0019] A method for automatically controlling equipment in an automotive vehicle is also disclosed. Sensitivity is determined for at least one semiconductor light sensor. Charge

incident on the light sensor is integrated to achieve the determined sensitivity. A discrete light signal is generated based on the light incident on the light sensor over the integration period. The discrete light signal can be analog or digital. In one embodiment, the discrete signal has a digital level with a variable, analog length. At least one vehicle equipment control signal is then generated based on the discrete light signal.

[0020] These and other objects, features, and advantages will be apparent from reading the following detailed description taken in connection with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0021] In the drawings:

[0022] Fig. 1 is a top plan view of an automotive vehicle that may incorporate automatically controlled equipment;

[0023] Fig. 2 is a top, rear perspective view of a rearview mirror including a forward ambient light sensor and a skyward ambient light sensor;

[0024] Fig. 3 is a top, rear perspective view of a rearview mirror circuit board for the rearview mirror according to Fig. 2;

[0025] Fig. 4 is a block diagram illustrating a generalized automatic vehicle equipment control system;

[0026] Fig. 5 is a schematic diagram of circuitry permitting control logic and a light sensor to be interconnected by a single line carrying both sensitivity control and sensor output;

[0027] Fig. 6 is a timing diagram illustrating operation of the circuitry of Fig. 5;

[0028] Fig. 7 is a timing diagram illustrating integration duration control and sensor output for a light sensor;

[0029] Fig. 8 is a schematic diagram illustrating operation of a light sensor having a pulse output;

[0030] Fig. 9 is a timing diagram illustrating operation of the light sensor of Fig. 8;

[0031] Fig. 10 is a schematic diagram illustrating operation of a light sensor with noise compensation;

[0032] Fig. 11 is a timing diagram illustrating operation of the light sensor of Fig. 10;

[0033] Fig. 12 is a schematic diagram illustrating an implementation of the light sensor of Fig. 14 using photodiodes as light transducers;

[0034] Fig. 12a is a circuit schematic of an alternate circuit for converting the LIGHT and NOISE signals of Fig. 12 to an output signal;

[0035] Figs. 13-16 are block diagrams illustrating various embodiments for light sensor packaging, output, and control;

[0036] Fig. 17 is a block diagram illustrating sensor logic for internally determining the integration period signal;

[0037] Fig. 18 is a block diagram illustrating the use of light transducers having different effective areas to achieve differing sensitivity;

[0038] Fig. 19 is a block diagram illustrating the use of light transducers having different apertures to achieve increased dynamic range;

[0039] Fig. 20 is a schematic diagram illustrating different transducer capacitances for different amounts of light-induced charge to achieve variable sensitivity;

[0040] Fig. 21 is a graph of the output potential as a function of accumulated incident light for the transducer of Fig. 20;

[0041] Fig. 22 is a schematic diagram illustrating a photodiode transducer incorporating an anti-bloom gate;

[0042] Fig. 23 illustrates an enclosure for a light sensor;

[0043] Fig. 24 illustrates a light sensor field of view as a function of light transducer distance from the lens;

[0044] Fig. 25 is a graph illustrating light sensor optical gain as a function of light transducer distance from the lens;

[0045] Fig. 26 is a perspective view illustrating an alternate light sensor;

[0046] Fig. 26a is a side elevation view illustrating of the sensor according to Fig. 26;

[0047] Fig. 27 is a graph illustrating frequency responses of the human eye;

[0048] Fig. 28 is a graph illustrating frequency response of a typical light transducer;

[0049] Fig. 29 is a drawing of an enclosure incorporating an infrared filter;

[0050] Figs. 30a-30d illustrate a side view of the light sensor die at four stages during the direct depositing of a film on a sensor transducer;

[0051] Fig. 31 is a graph of the frequency response of a window film that may be used to implement a light sensor filter;

[0052] Fig. 32 is a graph of the frequency response of a light sensor incorporating the window film with the frequency response shown in Fig. 31.

[0053] Fig. 33 is a block diagram illustrating circuitry for an automatically dimmed rearview mirror;

[0054] Fig. 34 is a block diagram illustrating a rearview mirror system with interior and exterior rearview mirrors;

[0055] Fig. 35 is a schematic diagram illustrating an embodiment of control logic for an automatically dimming interior rearview mirror;

[0056] Fig. 36 is a schematic diagram illustrating operation of electrochromic element transmittance control;

[0057] Fig. 37 is a timing diagram illustrating electrochromic element transmittance control;

[0058] Fig. 38 is a graph indicating dimmer reflectance as a function of dimmer control signal duty cycle;

[0059] Fig. 39 is a flow diagram illustrating operation of automatically dimming rearview mirror control logic;

[0060] Fig. 40 is a graph illustrating binary logarithmic approximation implemented in an embodiment of control logic for an automatically dimming rearview mirror;

[0061] Fig. 41 is a block diagram illustrating equipment for detecting the presence of moisture on a vehicle window;

[0062] Fig. 42 is a ray diagram illustrating moisture detection on an outside surface causing an increase in reflected light;

[0063] Fig. 43 is a ray diagram illustrating moisture detection on an outside surface causing a decrease in reflected light;

[0064] Fig. 44 is a flow diagram illustrating operation of control logic for automatically removing moisture from a vehicle window;

[0065] Fig. 45 is a block diagram illustrating circuitry for controlling headlamps;

[0066] Fig. 46 is a graph illustrating the differences in the spectral content of ambient light on a cloudy day and ambient light on a clear day;

[0067] Fig. 47 is a flow diagram illustrating operation of control logic for automatically controlling vehicle headlamps;

[0068] Fig. 48 is a chart illustrating wavelength responsivity of a filter that can be advantageously utilized for the headlight dimmer sky sensor;

[0069] Fig. 49 is a polar iso-candela plot of the light sensor according to Figs. 26 and 26a having a cylindrical lens; and

[0070] Fig. 50 is a rectangular iso-candela plot according to Fig. 49 viewed orthogonally to the longitudinal axis of the cylindrical lens.

DETAILED DESCRIPTION OF THE DRAWINGS

[0071] Referring now to Fig. 1, an automotive vehicle 20 is shown. Vehicle 20 is driven by operator 22. Operator 22 uses interior rearview mirror 24 and one or more exterior rearview mirrors 26 to view rearward scenes, shown generally by 28. Most of the time, operator 22 looks forward through windshield 30. The eyes of the operator 22 therefore adjust to forward ambient light 32 coming generally from the front of the vehicle. In low ambient light conditions, a relatively bright light source in rearward scene 28 may reflect from mirrors 24, 26 and temporarily visually impair, distract, or dazzle operator 22. This relatively strong light is known as glare 34.

[0072] To reduce the impact of glare 34 on operator 22, the reflectance of mirrors 24, 26 may be reduced. Manually adjustable interior mirrors contain a prismatic reflective element manually switched by operator 22 to change the amount of light that the mirror reflects to operator 22. Automatically dimming interior and exterior mirrors 24, 26 include elements that automatically adjust the amount of light reflected to operator 22 responsive to the detected

level of glare 34. Automatically dimming mirrors include a light sensor for glare 34 and, typically, a light sensor for forward ambient light 32.

[0073] Another environmental condition that can have an affect on operator 22 is moisture, which may condense on or impact vehicle windows, such as windshield 30 or rear window 36. Such moisture can impair the view of operator 22. This moisture may take the form of rain, snow, sleet, and the like on a window exterior surface, or may be fog, frost, ice, and the like on a window exterior or interior surface. Vehicle 20 typically includes several means for removing moisture, such as wipers 38 for windshield 30 and, possibly, rear window 36, defoggers 40 built into the dashboard of vehicle 20, and defrosters 42 built into rear window 36, or possibly windshield 30. Typically, this moisture-removing equipment is manually controlled. In order to automatically control such moisture-removing means, the presence of moisture on vehicle windows 30, 36 must be properly detected. Light sensors can be used to detect the moisture.

[0074] Other vehicle equipment requiring control are headlamps 44 that illuminate an area in front of vehicle 20 when ambient conditions do not provide sufficient light. Manual controls for the headlamps, parking lights, and bright lights, are well known. Headlamps 44 may also be automatically varied between off or daylight running light settings and nighttime light beams based on the level of ambient light detected by a light sensor (not shown in Fig. 1). Since most ambient light illuminating the forward view of operator 22 comes from above vehicle 20, skyward ambient light 46 from a direction generally in front of and above vehicle 20 may advantageously be monitored for automatically controlling headlamps 44.

[0075] Other vehicle equipment may also be controlled responsive to light sensors. Openings such as power door windows, sunroofs, moon roofs, convertible tops, and the like can be

automatically closed when rain is detected. Passenger compartment heating and cooling may be improved by anticipating changes in thermal loading, such as when the sun disappears behind or appears from a cloud bank, or detecting which side of the vehicle is in the sun. Parking lamps, puddle lights, courtesy lights, and other auxiliary lighting may be controlled based on ambient light levels, the detected presence of exterior moisture, the running state of vehicle 20, and the like. Fog lights on the front and/or rear of the vehicle can be controlled based on the detection of fog. While particular applications, such as rearview mirror dimming, moisture removal, and headlamp control, are described in detail herein, it is understood that the present invention applies to a wide variety of automatic equipment controls within automotive vehicles. Accordingly, as used herein, "vehicle equipment" refers to power windows, power doors, sunroofs, moon roofs, convertible tops, running lights, fog lights, parking lights, puddle lights, courtesy lights, and other vehicle lights, rearview mirrors, heating and cooling systems, windshield wipers, and headlamps, and any other controlled mechanism or components in a vehicle.

[0076] Regardless of the vehicle equipment controlled, automotive consumers will welcome such automated control of equipment provided the equipment meets certain criteria. First, the automatic equipment works in a reasonable and predictable manner throughout the wide range of operating conditions experienced by an automotive vehicle. Second, the equipment operates reliably throughout the life of the vehicle. Third, the equipment is reasonably priced. Fourth, the automatic equipment frees the driver to concentrate on driving.

[0077] The light sensors, and also possibly the control logic, for the automatically controlled equipment can be advantageously implemented in interior rearview mirror 24. The rearview mirror provides an excellent location for light sensors as it is located high in the vehicle

passenger compartment at a location with a good field of view through the side windows, the front windshield 30, and the rear window 36. The interior rearview mirror 24 includes a forward ambient light sensor 58 (Fig. 2); skyward ambient light sensors 150, 150', 158, 158'; glare sensor 62 (Fig. 3); light emitter 104; a first reflected light sensor 110; and a second reflected light sensor 110a. Interior rearview mirror 24 includes housing 850 into which these light sensors are assembled. Forward ambient light sensor 58 is held within housing 850 so as to view forward ambient light 32. One or more skyward ambient light sensors 150, 150', 158, 158' are held within housing 850 so as to view skyward ambient light 46. Although four skyward ambient light sensors are illustrated, a single sky sensor 150 or two light sensors 150, 158 can be used to monitor the forward sky ambient light 46.

[00078] Housing 850 may be formed so as to restrict light collected by skyward ambient light sensors 150, 150', 158, 158'. In particular, skyward ambient light sensors 150, 150', 158, 158' may be recessed into housing 850 to limit the amount of forward ambient light 32 received by light sensors 150, 150', 158, 158'. Similarly, forward ambient light sensor 58 may be recessed in housing 850 to limit the amount of skyward light 46 impacting this sensor. Housing 850 is attached to a mirror mounting assembly, shown generally by 852. Mounting assembly 852 includes mounting foot 854 for attaching rearview mirror 24 to windshield 30. The mounting foot may be attached to windshield 30 using a clear adhesive. Moisture sensor 120 may be incorporated into mounting foot 854. In addition or alternatively, one or more of forward ambient light sensor 58, skyward ambient light sensor 150, and skyward ambient light sensor 158 may be incorporated into mounting foot 854. By locating sensors 150, 158 in mounting foot 854, sensors 150', 158' can be omitted.

[0079] Referring now to Fig. 3, the rearview mirror circuit board assembly is shown. It is envisioned that either a single-sided or a two-sided conventional circuit board may be employed. Rearview mirror housing 850 encloses circuit board 860, carrying forward ambient light sensor 58, glare sensor 62, and skyward ambient light sensors 150, 150', 158, 158' on surface 861 of the board 860. Glare sensor 62 may be bent around circuit board 860 or may be connected to circuit board 860 by flexible wires to permit glare sensor 62 to view glare 34 from generally behind vehicle 20. Alternatively, if a two-sided circuit board is used, the glare sensor 62 may be mounted to the front of the circuit board. Moisture sensor 120 may include emitter 104 and one or more light sensors 110, 110a connected to circuit board 860 by cabling 862. Circuit board 860 may include control logic 66 receiving sensor signals and generating equipment control signals responsive thereto. Cable 863 supplies power and ground to circuit board 860 as well as carrying equipment control signals from circuit board 860 to the remainder of the vehicle electrical system. The cable 863 may be mounted to the circuit board via a conventional multi-pin connector 865.

[0080] An automatic vehicle equipment control circuit 165 is illustrated in block diagram form in Fig. 4, a portion of which is mounted in interior rearview mirror 24. The control circuit 165 includes an ambient light sensor 150, an optional ambient light sensor 158, an optional ambient light sensor 150', and optional ambient light sensor 158', a glare sensor 62, a forward ambient light sensor 58, a moisture sensor 110, an optional moisture sensor 110a, and an emitter 104 connected to control logic 66 through busses 164. It will be recognized that fewer sensors could be provided. Additional sensors, such as other light sensors, speed sensors and temperature sensors that are not illustrated, may also be connected to the control logic 66. Busses 164 connect each of the light sensors and the emitter to control logic 66. Control logic

66 may be responsive to light signals on busses 164 to generate equipment control signals on busses 166 so as to control various automotive vehicle equipment such as headlamps 44, wipers 38, a defogger 40, a defroster 42, and rearview mirrors 24, 26 automatically. Less or additional equipment could be controlled by the control logic 66. Light level signals on busses 164 and control signals on busses 166 may be analog, discrete, digital, or the like, to fit the particular need of the sensors and equipment. Although shown as a single box that can be located in the mirror housing 850, it will be recognized that the control logic 66 may be distributed throughout vehicle 20. It will be recognized that significant cost and manufacturing advantages can be achieved by implementing the control logic using a minimum number of components. Equipment control signal as used herein refers to a signal that is used in controlling equipment, which control signal can be used directly by the equipment or input to further circuitry which controls the equipment.

[0081] Somewhat more particularly, one or more of the light sensors 58, 62, 110, 110a, 150, 150', 158, 158' are implemented using a light sensor that incorporates a silicon-based light transducer and conditioning electronics, which is advantageously implemented on a single substrate. The light transducer generates charge at a rate proportional to the amount of incident light. This light-induced charge is collected over an integration period. The resulting potential on bus 164 is proportional to, and thus indicative of, the level of light to which the sensor is exposed over the integration period. Such a light sensor with integral charge collection has many advantages. For example, the ability to incorporate additional electronics on the same substrate as the transducer increases noise immunity and permits the sensor output to be formatted for use by a digital circuit. Component integration additionally reduces the system cost. Silicon devices are more temperature invariant than CdS cells and can be

packaged to provide protection from humidity, shock, and vibration. Additionally, silicon-based light sensors have a faster response time than CdS cells, speeding up the response time of the automatic equipment. One disadvantage of silicon-based light transducers is that they have a frequency response substantially different than that of the human eye. Types of charge accumulating light transducers include photodiodes and photogate transistors. A variety of charge integrating photodiode devices are known, including those in U.S. Patent Nos. 4,916,307 to Nishibe et al.; 5,214,274 to Yang; 5,243,215 to Enomoto et al.; 5,338,691 to Enomoto et al.; and 5,789,737 to Street. Photogate transistor devices are described in U.S. Patent Nos. 5,386,128 to Fossum et al. and 5,471,515 to Fossum et al.

[0082] The control logic 66 includes a controller that can advantageously be implemented using a microprocessor, microcontroller, digital signal processor, programmable logic unit, or the like. A PIC 16C620A microcontroller commercially available from Microchip may be used. The control logic receives light signals from sensors 58, 62, 110, 110a, 150, 150', 158, 158' responsive to which it determines a light level. The microcontroller need not include an analog-to-digital converter (ADC) connected to receive the output from the sensors 58, 62, 110, 110a, 150, 150', 158, 158' if these sensors produce discrete, digital outputs. The microcontroller may optionally include electronically alterable memory in which calibrated thresholds associated with each of the sensors are stored during manufacture of the mirror 24 for later use in controlling equipment 24, 26, 38, 40, 42 and 44 in a predetermined manner. The microcontroller in control logic 66 preferably generates control signals on lines 164 that select the sensitivity of the sensors 58, 62, 110, 110a, 150, 150', 158, 158' to effect a wide dynamic range for the light sensors. The microcontroller also generates control signals applied

to the automatically controlled equipment 24, 26, 38, 40, 42, and 44 responsive at least in part to the signals from the sensors.

[0083] Referring now to Fig. 5, the control logic 66 and sensors 58, 62, 110, 110a, 150, 150', 158, 158' will be described in greater detail. Light sensor 170 refers generally to a light sensor that can be used to implement any of the sensors 58, 62, 110, 110a, 150, 150', 158, 158'. The control logic 66 and light sensor 170 are interconnected by a single line 164 that carries interconnect signals 186, which can advantageously include both light sensor sensitivity control signals and resultant light sensor output signals. The microcontroller used to implement control logic 66 includes a transistor element Q1 and a buffer 192 connected to an output pin 188, or other input/output (I/O) pin structure, which is connected to signal line 164. The transistor element Q1 may be implemented using a suitable transistor, such as a field effect transistor (FET), connected between signal pin 188 and ground. Transistor Q1 is controlled by control line 190 connected to the base of transistor Q1. Buffer 192 is also connected to signal pin 188 to isolate the signal line 164 from signal levels present in the microcontroller.

[0084] Light sensor 170 includes enclosure 172 with window 174 admitting light 176 incident on exposed light transducer 178. Enclosure 172 admits power pin 180, ground pin 182, and signal pin 184. "Window" as used herein refers to a path by which light travels through the sensor package to reach the transducer surface, and thus could be an opening in an opaque semiconductor package, a transparent or translucent encapsulant, or the like. The use of only three pins 180, 182, 184 greatly reduces the cost of light sensor 170 and associated control logic 66.

[0085] Light sensor 170 is connected to control logic 66 through bus 164, which carries interconnection signal 186 between signal pin 184 in light sensor 170 and signal pin 188 in

control logic 66. As will be described below, signal pins 184, 188 are tri-state ports permitting interconnect signal 186 to provide both an input to light sensor 170 and an output from light sensor 170.

[0086] Within light sensor 170, transistor Q2, which can be implemented using a suitable transistor such as an FET element, is connected between signal pin 184 and ground. Transistor Q2 is controlled by output pulse 194 connected to the gate of Q2. Constant current source 196 is connected to signal pin 184 so that if neither transistor Q1 nor transistor Q2 is ON (high logic level), interconnect signal 186 is pulled to a high logic level. Constant current source 196 nominally sources about 0.5 mA to pull up interconnect signal 186. The input of Schmidt trigger inverter 198 is connected to signal pin 184. Inverters 200 and 202, which are connected in series, follow Schmidt trigger inverter 198. The output of inverter 202 clocks D flip-flop 204. The output of multiplexer 206 is connected to the D input of flip-flop 204. The select input of multiplexer 206 is driven by output pulse 194 such that when output pulse 194 is asserted, the D input of flip-flop 204 is unasserted, and when output pulse 194 is not asserted, the D input of flip-flop 204 is asserted. The output of NAND gate 208 is connected to low asserting reset 210 of flip-flop 204. The output of flip-flop 204 is integration pulse 212. Integration pulse 212 and the output of inverter 200 are inputs to NAND gate 208. Light-to-pulse circuit 214 accepts integration pulse 212 and the output of exposed light transducer 178 and produces output pulse 194.

[0087] Light sensor 170 may advantageously include a shielded light transducer 216, which does not receive light 176. Shielded light transducer 216 has substantially the same construction as exposed light transducer 178, being of the same size and material as transducer

178. Light-to-pulse circuit 214 uses the output of shielded light transducer 216 to reduce the effects of noise in exposed light transducer 178.

[0088] Referring now to Fig. 6, a timing diagram illustrating operation of the circuitry of Fig. 5 is shown. Initially, low asserting interconnect signal 186 is high. The state of flip-flop 204 must be zero for, if the state is one, both inputs to NAND gate 208 would be high, asserting reset 210 and forcing the state of flip-flop 204 to zero.

[0089] At time 220, control logic 66 asserts control line 190 turning transistor Q1 ON. Interconnect signal 186 is then pulled low at time 222. The output of inverter 202 transitions from low to high setting the state of flip-flop 204 to one (*i.e.*, a high logic level) which causes integration pulse 212 to become asserted at time 224. Light-to-pulse circuit 214 begins integrating light 176 incident on exposed light transducer 178. At time 226, control line 190 is brought low turning transistor Q1 off. The difference between time 226 and time 220 is integration period 228 requested by control logic 66. Since both transistors Q1 and Q2 are off, interconnect signal 186 is pulled high by current source 196 at time 230. Since the output of inverter 200 and integration pulse 212 are both high, reset 210 is asserted causing the state of flip-flop 204 to change to zero and integration pulse 212 to become unasserted at time 232.

This signals light-to-pulse circuit 214 to stop integrating light 176 incident on exposed light transducer 178.

[0090] At time 234, light-to-pulse circuit 214 asserts output pulse 194 to begin outputting light intensity information. Asserting output pulse 194 turns transistor Q2 on, pulling interconnect signal 186 low at time 236. This causes inverter 202 to output a low-to-high transition clocking a zero as the state of flip-flop 204. Light-to-pulse circuit 214 deasserts output pulse 194 at time 238. The difference between time 238 and time 234 is light intensity period 240

indicating the amount of light 176 incident on exposed light transducer 178 over integration period 228. Transistor Q2 is turned off when output pulse 194 goes low at time 238. Since both transistors Q1 and Q2 are off, interconnect signal 186 is pulled high at time 242. Buffer 192 in control logic 66 detects the transitions in interconnect signal 186 at times 236 and 242. The difference in time between times 242 and 236 is used by control logic 66 to determine the intensity of light 176 received by light sensor 170.

[0091] If shielded light transducer 216 is included in light sensor 170, the difference in time between the deassertion of integration pulse 212 at time 232 and the assertion of output pulse 194 at time 234 is due, in part, to the thermal noise in light sensor 170. This difference is expressed as thermal noise period 244. Thermal noise period 244 may be used by control logic 66 to determine the temperature of light sensor 170 or may be more simply used to determine when the noise level in sensor 170 is too high for a reliable reading. Control logic 66 may disable automatic control of vehicle equipment if the temperature of light sensor 170 exceeds a preset limit.

[0092] Fig. 7 illustrates a timing diagram of integration duration control and sensor output for a light sensor. Charge accumulating light sensor 170 exhibits increased sensitivity and increased dynamic range through variable integration periods. The total amount of light-induced charge, which can be effectively measured, is limited. Therefore, in the presence of bright light, a short integration time is desirable to prevent saturation. However, if a short integration time is used in low light conditions, the charge signal may be lost in noise inherent in sensor 170 (*i.e.*, the signal-to-noise ratio will be so low that the signal level will be undetectable).

[0093] Control line 190 includes a sequence of integration periods having varying lengths. In the example shown, short integration pulse 240 having short integration period 242 is generated. A semiconductor light sensor may output a short pulse in a completely dark environment due to noise. Therefore, any sensor output pulse 194, such as short signal pulse 244, having a duration less than a threshold is ignored by control logic 66. Next, medium integration pulse 246 having medium integration period 248 is generated. Resulting medium signal pulse 250 has a duration indicative of the amount of light incident on sensor 170 during medium integration period 248. Long integration pulse 252 having long integration period 254 is generated. If light sensor 170 is sufficiently bright, saturation will result. Therefore, long signal pulse 256 having a duration greater than a threshold is also ignored by control logic 66. The signal represented by control line 190 may be generated outside of light sensor 170, such as by control logic 66, or may be generated by sensor logic within light sensor 170. By varying the integration period, the sensitivity is adjusted. Varying the sensitivity by providing successive integration periods of different duration allows the appropriate sensitivity to be detected, and responsive thereto, selected. A significant advantage of the sensor having bi-directional interconnect signal 186 is that the control logic 66 can control the sensitivity of the sensor 170 to dynamically compensate for different light conditions by varying the integration periods for the sensor.

[0094] Referring now to Fig. 8, a schematic diagram illustrating operation of a light sensor having a pulse output is shown. Light-to-pulse circuit 300 includes exposed light transducer 178 for converting light 176 incident on exposed light transducer 178 into charge accumulated in light storage capacitor 304, indicated by C_{SL} . Exposed light transducer 178 may be any device capable of converting light 176 into charge, such as the photogate sensor described in

U.S. Patent No. 5,471,515 entitled "ACTIVE PIXEL SENSOR WITH INTRA-PIXEL CHARGE TRANSFER" to Eric Fossum et al. Preferably, light transducer 178 is a photodiode such as is described below. Except as noted, the following discussion does not depend on a particular type or construction for exposed light transducer 178.

[0095] Light-to-pulse circuit 300 also includes light-to-pulse circuit 214 (Fig. 8), which is connected to transducer 178, receives an integration pulse 212, and outputs a light comparator signal which is proportional to the amount of light 176 impacting transducer 178 during integration period pulse 212. Light-to-pulse circuit 214 operates under the control of sensor logic 306. Sensor logic 306 generates reset signal 308 controlling switch 310 connected between exposed light transducer output 312 and V_{DD}. Sensor logic 306 also produces sample signal 314 controlling switch 316 between exposed light transducer output 312 and light storage capacitor 304. The voltage across light storage capacitor 304, light storage capacitor voltage 318, is fed into one input of comparator 320. The other input of comparator 320 is ramp voltage 322 across ramp capacitor 324. Ramp capacitor 324 is in parallel with current source 326 generating current I_R. Sensor logic 306 further produces ramp control signal 328 controlling switch 330 connected between ramp voltage 322 and V_{DD}. Comparator 320 produces comparator output 332 based on the relative levels of light storage capacitor voltage 318 and ramp voltage 322. Sensor logic 306 may generate reset signal 308, sample signal 314, and ramp control signal 330 based on internally generated timing or on externally generated integration pulse 212.

[0096] Referring now to Fig. 9, a timing diagram illustrating operation of the light sensor of Fig. 8 is shown. A measurement cycle is started at time 340 when sample signal 314 is asserted while reset signal 308 is asserted. This closes switch 316 to charge light storage

capacitor 304 to V_{DD} as indicated by voltage level 342 in light storage capacitor voltage 318. Reset signal 308 is then deasserted at time 344, opening switch 310 and beginning integration period 346. During integration period 346, light 176 incident on exposed light transducer 178 generates negative charge causing declining voltage 348 in light storage capacitor voltage 318. At time 350, ramp control signal 328 is asserted closing switch 330 and charging ramp capacitor 324 so that ramp voltage 322 is V_{DD} as indicated by voltage level 352.

[0097] Sample signal 314 is deasserted at time 354, causing switch 316 to open, thereby ending integration period 346. At some time 356 following time 354 and prior to the next measurement cycle, reset signal 308 must be asserted closing switch 310. At time 358, ramp control signal 328 is deasserted opening switch 330. This causes ramp capacitor 324 to discharge at a constant rate through current source 326 as indicated by declining voltage 360 in ramp voltage 322. Initially, as indicated by voltage level 362, comparator output 332 is unasserted because ramp voltage 322 is greater than light storage capacitor voltage 318. At time 364, declining voltage 360 in ramp voltage 322 drops below light storage capacitor voltage 318 causing comparator output 332 to become asserted. Comparator output 322 remains asserted until time 366 when ramp control signal 328 is asserted closing switch 330 and pulling ramp voltage 322 to V_{DD} . The difference between time 366 and time 364, indicated by pulse duration 368, is inversely related to the amount of light 176 received by exposed light transducer 178 during integration period 346. The integration period 346 can be set directly by the integration pulse 212, or a signal derived from integration pulse 212. It is envisioned that the integration period 346 will be proportional to the width of the integration pulse 212, which is proportional to the pulse width of the control line signal 190 in the circuit of Fig. 5.

[0098] Referring now to Fig. 10, a schematic diagram illustrating operation of a light sensor with noise compensation is shown. A light-to-pulse circuit, shown generally by 380, improves upon light-to-pulse circuit 300 by incorporating shielded light transducer 216 and associated electronics. Shielded light transducer 216 preferably has the same construction as exposed light transducer 178. However, shielded light transducer 216 does not receive light 176. Charge generated by shielded light transducer 216, therefore, is only a function of noise. This noise is predominately thermal in nature. By providing shielded light transducer 216 having the same construction as exposed light transducer 178, such that the exposed and shielded transducers have the same surface area and material composition, and may be deposited on the same die, the noise signal produced by shielded light transducer 216 will closely approximate the noise within the signal produced by exposed light transducer 178. By subtracting the signal produced by shielded light transducer 216 from the signal produced by exposed light transducer 178, the effect of noise in light transducer 178 can be greatly reduced.

[0099] Reset signal 308 controls switch 382 connected between shielded transducer output 384 and V_{DD}. Sample signal 314 controls switch 386 connected between shielded transducer output 384 and noise storage capacitor 388 indicated by C_{SN}. The noise storage capacitor voltage 390, which is the voltage across noise storage capacitor 388, is one input to comparator 392. The second input to comparator 392 is ramp voltage 322. The outputs of comparator 392, noise comparator output 394, and comparator output 332, serve as inputs to exclusive-OR gate 396. Exclusive-OR gate 396 generates exclusive-OR output 398 indicating the intensity of light 176.

[0100] Referring now to Fig. 11, a timing diagram illustrating operation of the light sensor of Fig. 10 is shown. Light-to-pulse circuit 380 functions in the same manner as light-to-pulse circuit 300 with regard to reset signal 308, sample signal 314, light storage capacitor voltage

318, ramp voltage 322, ramp control signal 328, and comparator output 332. At time 340, sample signal 314 is asserted while reset signal 308 is asserted. Switches 382 and 386 are both closed charging noise storage capacitor 388 to V_{DD} as indicated by voltage level 410 in noise storage capacitor voltage 390. At time 344, reset signal 308 is deasserted opening switch 382 and causing declining voltage 412 in noise storage capacitor voltage 390 from charge produced by shielded light transducer 216 due to noise. At time 354, sample signal 314 is deasserted ending integration period 346 for noise collection. At time 358, ramp control signal 328 is deasserted causing declining voltage 360 in ramp voltage 322. Initially, as indicated by voltage level 414, noise comparator output 394 is unasserted because ramp voltage 322 is greater than noise storage capacitor voltage 390. Since comparator output 332 is also unasserted, output 398 from comparator 396 is unasserted as indicated by voltage level 416. At time 418, ramp voltage 322 drops below the level of noise storage capacitor voltage 390, causing noise comparator output 394 to become asserted. Since noise comparator output 394 and comparator output 332 are different, output 398 from comparator 396 is asserted. At time 364, ramp voltage 322 drops beneath the level of light storage capacitor voltage 318, causing comparator output 332 to become asserted. Since both noise comparator output 394 and comparator output 332 are now asserted, output 398 from exclusive-OR gate 396 now becomes unasserted. The difference between time 364 and time 418, output pulse duration 420, has a time period proportional to the intensity of light 176 incident on exposed light transducer 178 less noise produced by shielded light transducer 216 over integration period 346. The duration between time 418 and time 358, noise duration 422, is directly proportional to the amount of noise developed by shielded light transducer 216 over integration period 346. Since the majority of this noise is thermal noise, noise duration 422 is indicative of the temperature of shielded light

transducer 216. At time 366, ramp control signal 328 is asserted, deasserting both noise comparator output 394 and comparator output 332.

[0101] Referring now to Fig. 12, a schematic diagram of an implementation of the light sensor of Fig. 10 using photodiodes as light transducers is shown. Light-to-pulse circuit 380 is implemented using exposed photodiode 430 for exposed light transducer 178 and shielded photodiode 432 for shielded light transducer 216. The anode of exposed photodiode 430 is connected to ground and the cathode connected through transistor Q20 to V_{DD}. The base of transistor Q20 is controlled by reset signal 308. Hence, transistor Q20 functions as switch 310. Transistors Q21 and Q22 are connected in series between V_{DD} and ground to form a buffer, shown generally by 434. The base of transistor Q21 is connected to the collector of exposed photodiode 430. The base of load transistor Q22 is connected to fixed voltage V_B. The output of buffer 434 is connected through transistor Q23 to light storage capacitor 304. The base of transistor Q23 is driven by sample signal 314, permitting transistor Q23 to function as switch 316. The anode of shielded photodiode 432 is connected to ground and the cathode is connected to V_{DD} through transistor Q24. The base of transistor Q24 is driven by reset signal 308 permitting transistor Q24 to function as switch 382. Transistors Q25 and Q26 form a buffer, shown generally by 436, isolating the output from shielded photodiode 432 in the same manner that buffer 434 isolates exposed photodiode 430. Transistor Q27 connects the output of buffer 436 to noise storage capacitor 388. The base of transistor Q27 is driven by sample signal 314 permitting transistor Q27 to function as switch 386. Typically, light storage capacitor 304 and noise storage capacitor 388 are 2 pF. Ramp capacitor 324, typically 10 pF, is charged to V_{DD} through transistor Q28. The base of transistor Q28 is driven by ramp control signal 328 permitting transistor Q28 to function as switch 330. Ramp capacitor 324 is

discharged through current source 326 at an approximately constant current I_R of 0.01 μA when transistor Q28 is off.

[0102] Sensor power-up response is improved, and the effective dynamic range of the sensor is extended, by including circuitry to inhibit output if ramp voltage 322 drops beneath a preset voltage. Light-to-pulse circuit 380 includes comparator 438 comparing ramp voltage 322 with initialization voltage (V_{INIT}) 440. Comparator output 442 is ANDed with exclusive-OR output 396 by AND gate 444 to produce AND gate output 446. During operation, if ramp voltage 322 is less than initialization voltage 440, output 446 is deasserted (*i.e.*, is held to a low logic level). The use of comparator 438 and AND gate 444 guarantees that output 446 is not asserted regardless of the state of light-to-pulse circuit 380 following power-up. In a preferred embodiment, the initialization voltage is 0.45 V.

[0103] Sensor logic 306 generates control signals 308, 314, 328 based on integration pulse 212, which may be generated internally or provided from an external source. Buffer 447 receives integration pulse 212 and produces sample control 314. An odd number of sequentially connected inverters, shown generally as inverter train 448, accepts sample control 314 and produces reset control 308. A second set of odd-numbered, sequentially connected inverters, shown generally as inverter train 449, accepts reset signal 308 and produces ramp control signal 328. The circuit shown in Fig. 12 has a resolution of at least 8 bits and a sensitivity of approximately 1 volt per lux-second. The maximum output pulse duration 420 is independent of integration period 346 provided by the duration of integration pulse 212.

[0104] It is envisioned that the light signal 318 across capacitor 304 in Fig. 12 and the noise signal 390 across capacitor 388 may be input to differential operational amplifier 321 (Fig. 12a). The output of differential amplifier 321 is an analog signal representative of the

difference between the light signal 318 and the noise signal 390. This circuit can be used where the control logic 66 includes an analog-to-digital converter that can convert these digital signals to analog signals.

[0105] Referring now to Figs. 13-16, various embodiments for light sensor packaging, output, and control are shown. Each embodiment may include light-to-pulse circuitry as described above. In Fig. 13, light sensor package 450 accepts four pins for supply voltage V_{DD} , ground, sensitivity control signal 452, and output signal 454. Sensitivity control signal 452 may be integration pulse 212 used by light-to-pulse circuit 380 to produce output 398, which is sent as output signal 454. In Fig. 14, light sensor package 456 requires only three pins for V_{DD} , ground, and combined sensitivity control and output signal 458. Combined signal 458 may be interconnect signal 186 as described above. In Fig. 15, light sensor package 460 admits three pins for output signal 454, ground, and combined V_{DD} and sensitivity control signal 462. As is known in the art, combined signal 462 may be separated into power supply voltage V_{DD} and sensitivity control signal 452 through the use of filters. For example, a low-pass and high-pass filter can be used to separate the signals. In Fig. 16, light sensor package 464 admits three pins for V_{DD} , ground, and output signal 454. Sensitivity control signal 452 is generated within light sensor package 464 as described below.

[0106] Referring now to Fig. 17, a block diagram of sensor logic for determining the integration period signal within sensor 170 is shown. Sensor logic 306 may include free-running counter 470 driven by internal oscillator 472. Counter 470 may have taps, one of which is indicated by 474, connected to different counter bits. For example, one tap 474 may be connected to the n^{th} bit, the next tap 474 to the $n^{th}+2$ bit, the next tap 474 connected to the $n^{th}+4$ bit, and so on, with each successive tap thereby providing a pulse with a period four

times longer than the preceding tap 474. Sensor control signal generator 476 controls switch 478 to determine which tap 474 will be used to produce integration pulse 212. Typically, sensor control signal generator 476 sequences through each tap 474 repeatedly. Sensor control signal generator 476 then uses integration pulse 212 to generate control signals such as reset signal 308, sample signal 314, and ramp control signal 328 as described above. It will be recognized that where the sensor generates the integration pulse internally to vary the sensor sensitivity, although the control logic will be unable to alter the integration period, the control logic will receive short, intermediate, and long output pulses from the sensor responsive to which measured light levels can be determined bright, intermediate and low light levels.

[0107] Referring now to Fig. 18, an alternate embodiment of the light sensor is illustrated wherein light transducers having different effective areas are used to achieve variable sensitivity. As an alternative to varying the integration time, or together with varying the integration time, pairs of exposed light transducer 178 and shielded light transducer 216 having different effective areas may be used. If photodiodes 430, 432 are used as light transducers 178, 216, the effective area is the photodiode collector area. Small exposed light transducer 490 produces charge, which is converted to a voltage by light-to-voltage circuit 492. Light-to-voltage circuit 492 may be implemented using switches 310, 316, and light storage capacitor 304 as described above. Charge produced by small shielded light transducer 494 is converted to voltage by noise-to-voltage circuit 496. Noise-to-voltage circuit 496 may be implemented using switches 382, 386 and noise storage capacitor 388 as described above. The outputs of light-to-voltage circuit 492 and noise-to-voltage circuit 496 are converted to a pulse by voltage-to-pulse circuit 498, with a width based on charge accumulated over an integration period by small exposed light transducer 490 less charge due to noise integrated by small shielded light

transducer 494. Voltage-to-pulse circuit 498 may be implemented using comparators 320, 392, capacitor 324, current source 326, and gate 396 as described above. Medium exposed light transducer 500 has an effective area larger than the effective area for small exposed light transducer 490, resulting in increased sensitivity. For example, if the effective area of medium exposed light transducer 500 is four times larger than the effective area of small exposed light transducer 490, medium exposed light transducer 500 will be four times more sensitive to light 176 than will be small exposed light transducer 490. Medium shielded light transducer 502 has an effective area the same as medium exposed light transducer 500. Additional light-to-voltage circuit 492, noise-to-voltage circuit 496, and voltage-to-pulse circuit 498 produce a noise-corrected output pulse with a width based on light 176 incident on medium exposed light transducer 500 over the integration period. Similarly, large exposed light transducer 504 and large shielded light transducer 506 provide still increased sensitivity over medium exposed light transducer 500 and medium shielded light transducer 502 by having still greater effective area.

[0108] Switch 508 under the control of sensor logic 306 sets which output from voltage-to-pulse circuits 498 will be used for output signal 454. Output signal 454 may be selected based on a signal generated within sensor logic 306 or may be based on a signal provided from outside of sensor logic 306. In particular, a control signal may be provided by control logic 66 that controls switch 908 to select one of the small, medium, and large light transducers for connection to output 454.

[0109] In an alternative embodiment, only one shielded light transducer 216 is used. The output of shielded light transducer 216 is scaled prior to each noise-to-voltage circuit 496 in proportion to the varying effective areas of exposed light transducers 178. It will be

recognized by one of ordinary skill in the art that although the examples shown in Fig. 22 have three pairs of exposed light transducers 178 and shielded light transducers 216, any number of pairs may be used.

[0110] Referring now to Fig. 19, a block diagram illustrating the use of light transducers having different apertures to achieve increased dynamic range is shown. As an alternative to or together with specifying the integration period, exposed light transducers 178 having the same effective area may each have a different aperture admitting area for admitting light 176. Varying apertures may be produced using partial shield 520 blocking light 176 from reaching a portion of exposed light transducer 178. Each exposed light transducer 178 produces charge converted to a voltage by a corresponding light-to-voltage circuit 492. Switch 522 under the control of sensor logic 306 selects which light-to-voltage circuit 492's output is connected to voltage-to-pulse circuit 498. Voltage-to-pulse circuit 498 produces output signal 454 compensated for noise sensed by shielded light transducer 216 and processed by noise-to-voltage circuit 496. Sensor logic 306 may select output of light-to-voltage circuits 492 based on an internally generated control signal or on a control signal received from control logic 66.

[0111] In light sensor 170 with multiple transducers 178, 490, 500, 504, light sensor 170 detects incident light within a target spatial distribution. Each transducer 178, 490, 500, 504 views the same target spatial distribution. Hence, control logic 66 generates at least one equipment control signal 166 based on light signal 164 without mapping light signal 164 to an area within the target spatial distribution.

[0112] Referring now to Fig. 20, a transducer that can be used to achieve variable sensitivity is shown. A photodiode, shown generally by 530, is formed by *n*-type diffusion 532 in *p*-type substrate 534. Light 176 incident on photodiode 530 generates charge 536 which may be

accumulated in photodiode well 538 beneath n -type diffusion 532. Photodiode 530 has intrinsic photodiode capacitance C_{PD} . Floating diffusion 540 is also formed by diffusing n -type material in substrate 534. Floating diffusion 540 is connected through transistor Q20 to reset voltage V_{RESET} . The gate of transistor Q20 is connected to reset signal 308 under the control of sensor logic 306. Floating diffusion 540 is also connected to the input of buffer 542. The output of buffer 542 is transducer output V_{OUT} . Floating diffusion 540 defines diffusion well 544 formed in a region of substrate 534 when reset signal 308 is asserted. Floating diffusion 540 has an intrinsic floating diffusion capacitance C_{FD} . Transmission gate 546 is positioned between diffusion 532 and floating diffusion 540. Transmission gate 546 is held at voltage V_{TG} to form transmission well 548 thereunder. Transmission well 548 has a depth shallower than photodiode well 538 and diffusion well 544. Transmission gate 546 has an intrinsic transmission gate capacitance C_{TG} .

[0113] When reset signal 308 is asserted, bringing floating diffusion 540 to V_{RESET} , charge is eliminated in diffusion well 544. Further, when charge is reset in diffusion well 544, any charge 536 in photodiode well 538 above the depth of transmission well 548 flows through transmission well 548, through floating diffusion 540, and is eliminated. During a light integration period, reset signal 308 is unasserted, causing the voltage of floating diffusion 540 to float based on the amount of charge 536 in diffusion well 544. As light 176 strikes diffusion 532, charge 536 is created. Since charge 536 in photodiode well 538 up to the level of transmission well 548 was not eliminated by charge reset, additional charge 536 produced by incident light 176 flows from photodiode well 538 through transmission well 548 and into diffusion well 544. At charge level 550, beneath the level of transmission well 548, only diffusion well 544 is filling with charge 536. Hence, the voltage of floating diffusion 540 is

inversely proportional to floating gate capacitance C_{FD} . When enough charge 536 has been generated to fill diffusion well 544 above the level of transmission well 548 such as, for example, level 552, diffusion well 544, transmission well 548, and photodiode well 538 all fill with charge 536. Hence, the voltage of floating diffusion 540 is now inversely proportional to the sum of floating diffusion capacitance C_{FD} , transmission gate capacitance C_{TG} , and photodiode capacitance C_{PD} . The result is light sensor 170 with a sensitivity determined from the magnitude of the resulting light signal.

[0114] Referring now to Fig. 21, a graph of output potential as a function of accumulated incident light for the transducer of Fig. 20 is shown. A curve, shown generally by 554, shows transducer output V_{OUT} as a function of light 176 incident on diffusion 532 and, possibly, floating diffusion 540 over the integration period. During steep portion 556, charge 536 is accumulating in diffusion well 544 alone. Since the conversion gain is based only on floating diffusion capacitance C_{FD} , photodiode 530 appears to have a high sensitivity to incident light 176. During shallow portion 558, charge 536 is accumulated in diffusion well 544, transmission well 548, and photodiode well 538. Since the conversion gain is now dependent on the parallel combination of capacitances C_{FD} , C_{TG} , and C_{PD} , photodiode 530 now appears less sensitive to incident light 176. By adjusting voltages V_{RESET} and V_{TG} , knee point 559 between steep portion 556 and shallow portion 558 can be shifted affecting the dynamic range. For example, if the maximum voltage swing for floating diffusion 540 is 1 volt; the ratio of C_{FD} to the sum of C_{FD} , C_{TG} , and C_{PD} is 1:100; and knee point 559 is set at 0.5 volts, the dynamic range of photodiode 530 is increased about 50 times over the dynamic range of a similar photodiode without dual capacitance.

[0115] Referring now to Fig. 22, a schematic diagram illustrating a photodiode transducer incorporating an anti-bloom gate is shown. Anti-bloom gate 560 is formed between diffusion 532 and source voltage diffusion 562 tied to V_{DD}. Anti-bloom gate 560 is tied to anti-bloom voltage V_{AB}. Anti-bloom gate 560 forms anti-bloom well 564 in substrate 534 between photodiode well 538 and source diffusion well 566. Anti-bloom voltage V_{AB} is less than transmission gate voltage V_{TG} well 564, making anti-bloom well 564 shallower than transmission well 548. When accumulated charge generated by photodiode 530 exceeds charge level 568 equal to the depth of anti-bloom well 564, the excess charge flows beneath anti-bloom gate 560 into source voltage diffusion 562 and is eliminated. Anti-bloom gate 560 prevents output voltage V_{OUT} from dropping below a level detectable by comparator 320 in light-to-pulse circuit 380.

[0116] Referring now to Fig. 23, a drawing illustrating enclosure for a light sensor is shown. Light sensor 170 includes enclosure 172 having window 174 for admitting light, one ray of which is indicated by 570. Enclosure 172 admits power pin 180, ground pin 182, and signal pin 184. Semiconductor die 572, encapsulated within enclosure 172, incorporates light transducers 178, 216 and associated electronics as described above. Pins 180, 182, 184 may be wire bonded to die 527, as shown by wire 574 for power pin 180 and wire 576 for signal pin 184, or may be directly bonded to die 527, as shown for ground pin 182.

[0117] Enclosure 172 may be the same type used to construct three-terminal light emitting diodes (LEDs). A preferred format is commonly referred to as the T-1¾ or 5 mm package. Encapsulating electronics in such packages is well known in the art of optical electronics manufacturing.

[0118] A lens, shown generally by 578, is preferably used to focus light onto exposed light transducer 178. Lens 578 may be provided by a separate discrete lens positioned between light sensor 170 and the source of light rays 570, or the lens may be integral with the encapsulant 172 as shown in Fig. 27. In either case, lens 578 defines the field of view of light sensor 170 and provides improved sensitivity through optical gain. The lens can define the sensor field to have a narrow or wide angle.

[0119] Referring now to Fig. 24, a graph illustrating the light sensor field of view as a function of light transducer distance from the lens is shown. The field of view for exposed light transducer 178 in light sensor 170 is defined as view angle θ made by marginal ray 570 with respect to optical axis 580 through exposed light transducer 178. The half-angle field of view for spherical lens 578 is expressed by Equation 1:

$$\theta = 90 - \arccos\left\{r/R\right\} + n_2/n_1 * \sin\left\{\arccos\left\{r/R\right\} - \arctan\left\{(d - (R - (R^2 - r^2)^{1/2})) / r\right\}\right\}$$

[0120] where r is the lens aperture radius, R is the radius of curvature of lens 578, n_2 is the index of refraction of material within enclosure 172, n_1 is the index of refraction outside of enclosure 172, d is the distance from the center of lens 578 to exposed light transducer 178, and θ is measured in degrees. Typically, T-1 $\frac{3}{4}$ enclosure 172 is filled with epoxy and sensor 170 operates in air making the ratio of n_2 to n_1 approximately 1.5. Curve 590 plots half angle field of view θ as a function of distance d for a T-1 $\frac{3}{4}$ enclosure having a spherical lens 578 with radius R of 5.0 mm. As light transducer 178 moves farther from lens 578, the field of view decreases.

[0121] Referring now to Fig. 25, a graph illustrating light sensor optical gain as a function of light transducer distance from the lens is shown. Assuming paraxial approximation for rays

570, the optical gain of lens 578 can be estimated by considering the ratio of additional optical energy collected by light transducer 178 with lens 578 to the optical energy collected by light transducer 178 without lens 578. This can be computed by considering a cone of light with a base at the surface of lens 578 and a point at the focal point f of lens 578. The optical gain G may then be expressed as a function of the ratio of the cross section of the cone to the area of light transducer 178 which reduces to Equation 2:

$$G = f^2 / (f-d)^2$$

[0122] Curve 600 shows optical gain G as a function of distance d for a T-1 $\frac{3}{4}$ enclosure having a spherical lens 578 with radius R of 5.0 mm and a focal length f of 15.0 mm. As light transducer 178 moves farther from lens 578, the optical gain increases.

[0123] For use in automatically dimming rearview mirrors, the distance d between lens 578 and light transducer 178 can be adjusted for optimal performance of forward ambient light sensor 58 and glare sensor 62. Forward ambient light sensor 58 should have a wide field of view but need not be as sensitive as glare sensor 62. Glare sensor 62 should have a narrower field of view but must be more sensitive and, therefore, benefits from a higher optical gain. For the lens described above, a distance d of between 2 mm and 3 mm is suitable for forward ambient light sensor 58 and a distance d of between 6 mm and 7 mm is suitable for glare sensor 62. In addition to modifying lens parameters, other lens types such as aspheric, cylindrical, and the like are possible within the spirit and scope of the present invention.

[0124] Referring now to Fig. 26, an alternate light sensor 170' having an alternate encapsulant shape is illustrated. Light sensor 170' has enclosure 172 with curved surface 174 formed as an asymmetrical cylindrical lens, shown generally by 604. Lens 604 may have any desired radius and length and may, for example, have a radius r (Fig. 30a) of 1.25 mm and a length of 5 mm.

When mounted in the vehicle with the longitudinal axis of the cylindrical lens 604 generally normal with the horizon, horizontal compression without corresponding vertical compression is achieved. This permits observance of a wide region of the sky without sensing a correspondingly wide region of the ground, the vehicle roof, or the vehicle hood, when the sensor is used for the sky sensor. Conversely, when mounted horizontally, a wide horizontal view input is achieved. This characteristic can be advantageously used to implement the glare sensor, as described in greater detail herein below. Additionally, as used herein, light sensor 170 will generally refer to any light sensor shape, such as the spherical light sensor encapsulant of Fig. 23, and the cylindrical light sensor encapsulant of Fig. 26 unless indicated otherwise.

[0125] Referring now to Fig. 27, a graph illustrating frequency response of the human eye is shown. Curve 610 represents the relative photopic or daylight frequency response of the human eye. Curve 612 represents the relative scotopic or night frequency response of the human eye. In addition to being more sensitive to light intensity, scotopic response 612 is shifted more towards violet than photopic response 610.

[0126] Referring now to Fig. 28, a graph illustrating frequency response of a typical light transducer is shown. The relative frequency response of a typical photodiode light transducer 178 is shown as curve 620. When compared to photopic response curve 610 or scotopic response curve 612, the frequency response of exposed light transducer 178 contains significantly more infrared sensitivity. Depending upon the application, a filter may be placed before or incorporated into sensor 170 so that the output of exposed light transducer 178 more closely resembles a desired frequency response. The type of filtration required for light sensor 170 will depend on the application in which the sensor is used.

[0127] Referring now to Fig. 29, a light sensor package wherein the enclosure incorporates a filter is shown. Window 174 in enclosure 172 includes filter 630 operative to attenuate some components of light rays 570 striking exposed light transducer 178. For example, filter 630 may be an infrared filter such as a hot mirror commercially available from Optical Coating Laboratories, Inc. of Santa Rosa, California. A lens (not shown) may be placed in front of infrared filter 630 to control the image focused on the transducer. Other examples of filters are described in U.S. Patent Nos. 4,799,768 to Gahan and 5,036,437 to Macks.

[0128] It is envisioned that the filter 630 could be provided for the sensor 170 using other constructions. For example, a separate filter (not shown) can be mounted in a common housing with the sensor 170 at a position in front of the light sensor 170. For example, thin glass bandpass filters, such as the BG28 or BG18 filters commercially available from Schott Glass Technologies, Inc. of Duryea, Pennsylvania, could be employed. These filters reduce the infrared sensitivity of light sensor 170. In yet another embodiment, the spectral characteristics of light sensor 170 may be modified by material embedded into enclosure 172, or a thin appliqu  attached to the surface of the sensor encapsulant using an adhesive, or by directly depositing a filter onto semiconductor die 572.

[0129] A method by which an interference filter can be directly deposited onto a semiconductor light sensor 170 will now be described with respect to Figs. 30a through 30d. In the first step, a photoresist is deposited over the entire wafer. The photoresist may be any suitable commercially available photoresist material. The photoresist is then patterned to cover only those areas on the surface of the wafer requiring protection from the optical coating deposition such as the bonding pad, as shown in Fig. 30b. The optical film coating 579 is then applied to the surface of the die 572 as shown in Fig. 30c. The thin film 579 is

deposited directly on the light sensor in multiple layers. The first layer of the interference filter can be a silicon layer 50 to 80 nm thick, and preferably 65 nm thick. The second layer of the interference filter is a layer of silicon dioxide, 100 to 200 nm thick, and preferably 145 nm thick. The third layer of the interference filter is a silicon layer 50 to 80 nm thick, and preferably 60 nm thick. The fourth layer of the interference filter is a layer of silicon dioxide 100 to 200 nm thick, and preferably 140 nm thick. The fifth layer of the interference filter is a thick layer of silicon dioxide to provide protection, and may be 200 to 500 nm thick. After all five layers are deposited, the photoresist is lifted off using a conventional lift-off process, leaving the film deposited over the light sensitive region, but not over the bonding pads, as shown in Fig. 30d. The resulting die can be encapsulated to provide conventional packaging, such as the T 1-3/4 package of Fig. 23. The interference filter described above will filter light above 650 nm. Other materials could be applied in a similar manner to provide other filter characteristics.

[0130] Referring now to Fig. 31, a graph of the frequency response of a window film that may be applied to the light sensor filter is shown. A film with desired frequency characteristics such as, for example, XIR-70 from Southwall Technologies of Palo Alto, California, may be placed onto the window of light sensor 170. The spectrum of such a film is shown by curve 640. An adhesive such as, for example, 9500 PC from 3M Corporation of Minnesota, is affixed to the film. This adhesive film may then be attached to the surface of light sensor 170. Referring now to Fig. 32, curve 650 illustrates the response of light sensor 170 onto which has been placed an adhesive film having the frequency response shown by curve 640 in Fig. 31.

[0131] Having described the general system operation as well as describing the sensor in detail, the system will now be described in greater detail through some specific examples.

Referring first to Fig. 33, an automatically dimmed rearview mirror 24, 26 is shown that employs a light sensor. A dimming element, shown generally by 50, includes variable transmittance element 52 and reflective surface 54. Dimming element 50 is constructed such that reflective surface 54 is viewed through variable transmittance element 52. Dimming element 50 exhibits variable reflectance of light in response to dimming element control signal 56. Forward ambient light sensor 58 is positioned to receive forward ambient light 32 from generally in front of vehicle 20. Forward ambient light sensor 58 produces discrete ambient light signal 60 indicating the amount of forward ambient light 32 incident on forward ambient light sensor 58 over an ambient light integration period. Ambient light can be measured using the cyclical, varying integration periods shown in Fig. 7. Glare sensor 62 is positioned to detect glare 34 from generally behind vehicle 20 and may optionally be placed to view glare 34 through variable transmittance element 52. Glare sensor 62 produces discrete glare signal 64 indicating the amount of glare 34 incident on glare sensor 62 over a glare integration period. Control logic 66 receives ambient light signal 60 and determines an ambient light level. Control logic 66 determines the glare integration period based on the level of forward ambient light 32. Control logic 66 receives glare signal 64 and determines the level of glare 34. Control logic 66 outputs dimming element control signal 56, setting the reflectance of dimming element 50 to reduce the effects of glare 34 perceived by operator 22.

[0132] Either one of glare sensor 62 and forward ambient light sensor 58 or preferably both sensors are implemented using a semiconductor light sensor with variable sensitivity. Such sensors include light transducers that convert incident light into charge as described herein above. This charge is collected over an integration period to produce a potential that is converted by sensor 58, 62 into a discrete digital output that does not require analog-to-digital

conversion prior to digital processing in control logic 66. Eliminating the ADC conversion reduces the cost of the microprocessor. As can be seen from Fig. 11, the light-to-pulse converter introduces a delay. The delay is the time difference between the sample period and the beginning of the measurement period 420. This delay can be avoided using the analog circuit of Fig. 16a. However, the use of the analog circuit increases in two ways. First, the number of wires in bus 164 may be doubled as a first wire will be used for the integration period input signal and a second wire will be used as the analog output signal from differential amplifier 321. Secondly, the control logic will require an ADC to convert this analog signal to a digital signal usable by the digital control logic. Regardless of whether a digital or analog signal is generated, as mentioned above, one difficulty with silicon-based sensors is the difference in spectral sensitivity between silicon and the human eye. Accordingly, light filter 68 may be placed before or incorporated within ambient light sensor 50. Similarly, glare filter 70 may be placed before or incorporated within glare sensor 62.

[0133] Filters 68, 70 attenuate certain portions of the spectrum that may include visible light, infrared, and ultraviolet radiation such that light striking sensors 58, 62 combines with the frequency response of light transducers within sensors 58, 62 to more closely approximate the response of the human eye and to compensate for tinting in vehicle windows such as windshield 30. For an automatically dimming rearview mirror, an important goal is to decrease the glare experienced by vehicle operator 20 in low light conditions. In order to preserve night vision, which degrades rapidly when exposed to bright light, particularly in the range of scotopic curve 612, exposed light transducer 62, 58 should have a frequency response similar to scotopic curve 612 such that the mirror attenuate light that would otherwise negatively impact the night vision of the vehicle operator 22. If this filter is not used, exposed

light transducer 62, 58 should at least have an attenuated infrared response. This is increasingly more important as high intensity discharge (HID) headlamps, which emit more bluish light than do incandescent or halogen lamps, gain in popularity. Accordingly, the filters 68 and 70 preferably provide a filter characteristic similar to scotoptic curve 612.

[0134] Variable transmittance element 52 may be implemented using a variety of devices, as mentioned above. Dimming may be accomplished mechanically, using liquid crystal cells, suspended particle devices, or advantageously using an electrochromic cell that varies transmittance in response to an applied control voltage. As will be recognized by one of ordinary skill in the art, the present invention does not depend on the type or construction of dimming element 50. If dimming element 50 includes electrochromic variable transmittance element 52, reflective surface 54 may be either incorporated into or external to variable transmittance element 52.

[0135] Each interior rearview mirror 24 and exterior rearview mirror 26 must include dimming element 50 for automatic dimming. Preferably, interior rearview mirror 24 also includes control logic 66, light sensors 58, 62, and, if used, filters 68 and 70.

[0136] Referring now to Fig. 34, a block diagram of a rearview mirror system with interior and exterior rearview mirrors according to embodiments of the present invention is shown. Dimming element 50 in interior rearview mirror 24 operates as described above. Each exterior rearview mirror 26 includes exterior dimming element 80 having exterior variable transmittance element 82 operative to attenuate light from rearward scene 28 both prior to and after reflecting from exterior reflective surface 84. Exterior dimming element 80 provides variable reflectance based on exterior dimming element control signal 86. Exterior dimming element 80 may operate in any manner described with regard to dimming element 50 and,

preferably, is an electrochromic mirror. Exterior mirror control 88 generates exterior dimming element control signal 86. Exterior mirror control 88 may be part of exterior rearview mirror 26, interior rearview mirror 24, or may be located outside of any mirror 24, 26. Various embodiments for controlling exterior dimming element 80 depend on the amount of sensing and control to be included within exterior rearview mirror 26.

[0137] In one embodiment, control logic 66 in interior rearview mirror 24 determines exterior dimming element control signal 86 based on output from forward ambient light sensor 58 and glare sensor 62. Exterior dimming element control signal 86 may be generated directly by control logic 66 or exterior mirror control 88 may generate exterior dimming element control signal 86 based on a reflectance level calculated in control logic 66 and transmitted to exterior mirror control 88 through inter-mirror signal 90.

[0138] In another embodiment, exterior rearview mirror 26 includes exterior glare sensor 92 positioned to receive glare 34 from rearward scene 28 and operative to output exterior glare signal 94 based on the amount of glare 34 incident on glare sensor 92 over a glare integration period. Control logic 66 uses exterior glare signal 94 and ambient light signal 60 to determine the reflectance level for exterior dimming element 80. Again, exterior dimming element control signal 86 may be generated directly by control logic 66 or may be developed by exterior mirror control 88 based on the reflectance level contained in inter-mirror signal 90. Exterior glare filter 96, similar to glare filter 70, may be placed before exterior glare sensor 92 or built into exterior glare sensor 92 to provide exterior glare sensor 92 with a response closer to the response of the human eye. Inter-mirror signal 90 and exterior glare signal 94 may be in the form of a pulse width modulated signal, pulse density signal, serial data stream, or digitized and communicated over an automotive bus such as the CAN bus.

[0139] In still another embodiment, exterior glare sensor 92 produces exterior glare signal 98 routed directly to exterior mirror control 88. Exterior mirror control 88 determines exterior dimming element control signal 86 based on exterior glare signal 98 and the level of forward ambient light 32 determined by control logic 66 and sent to exterior mirror control 88 through inter-mirror signal 90.

[0140] In yet another embodiment, exterior rearview mirror 26 determines reflectance for exterior dimming element 80 independent of glare 34 or forward ambient light 32 sensed by interior rearview mirror 24. In this embodiment, exterior rearview mirror 26 operates as described above with respect to interior rearview mirror 24.

[0141] Referring now to Fig. 35, a schematic diagram illustrating an embodiment of control logic for an automatically dimming interior rearview mirror is shown. The circuit represents an effective yet inexpensive implementation for automatically dimming interior rearview mirror 24. Similar logic may be used to implement automatically dimming exterior mirror control, headlamp control, moisture detection and moisture removal control, electric window control, heating and cooling control, and the like. Control logic 66 utilizes a small, low cost microcontroller, indicated by U1, such as the PIC16C620A from Microchip Technology, Inc. of Chandler, Arizona. Forward ambient light sensor 58 communicates with microcontroller U1 through interconnection signal 186 connected to microcontroller input RB0. Similarly, glare sensor 62 communicates with microcontroller U1 through separate interconnection signal 186a connected to microcontroller input RB2. As described above, each interconnection signal 186 carries integration period 158 from microcontroller U1 to light sensor 58, 62 as well as light intensity period 240 from light sensor 58, 62 to microcontroller U1. Resistor R29 and

capacitor C4 connected between V_{DD} and ground provide filtered power for light sensors 58, 62.

[0142] Parallel resistor R15 and diode D5 are connected between V_{DD} and node 708.

Capacitor C12 is connected between node 708 and ground. Resistor R6 connects common node 708 to input /MCLR of microcontroller U1. Components D5, R15, R6, and C12 form a power-on reset circuit shown generally by 710. Power is supplied to control logic 66 through ignition line 712. Diode D1 protects from reversed polarity on ignition line 712 and diode D2 clamps the voltage derived from ignition line 712 to approximately 5 volts. Capacitors C2, C7 and C11, resistor R3, and ferrite element E1 form a power conditioning circuit shown generally by 714. Reverse line 716 is asserted when vehicle 20 is placed into reverse. Capacitor C10 and resistors R8, R9, and R27 form a reverse signal conditioning circuit, shown generally by 718. Reverse signal conditioning circuit 718 low pass filters reverse line 716 and provides electrostatic discharge protection for digital input pin RB6 on microcontroller U1. Microcontroller U1 uses the signal on reverse line 716 to clear variable transmittance element 52 whenever vehicle 20 is placed in reverse. Microcontroller U1 is clocked by an RC oscillator formed by resistor R2 connected between the OSC1 pin and V_{DD} and capacitor C1 connected between the OSC1 pin and ground. Resistor R30 and LED D3 connected in series between V_{DD} and open drain output RA4 of microcontroller U1 form an indicator lamp that may be mounted on interior rearview mirror 24 to alert operator 22 of the operating state of control logic 66. Switches S1 and S2 are connected to digital inputs RB1 and RB3, respectively, of microcontroller U1 to permit selecting control options.

[0143] Referring now to Fig. 36, a schematic diagram illustrating operation of electrochromic dimmer control is shown. A portion of control logic 66 has been redrawn to more clearly

illustrate control of electrochromic variable transmittance element 52. Electrochromic variable transmittance element 52 can be implemented using any suitable variable reflectance device, and may, for example, comprise the electrochromic element described in U.S. Patent No. 4,902,108 entitled “SINGLE-COMPARTMENT, SELF-ERASING, SOLUTION-PHASE ELECTROCHROMIC DEVICES, SOLUTIONS FOR USE THEREIN, AND USES THEREOF” issued to Byker. Electrochromic variable transmittance element 52 darkens in response to a control voltage applied at input node 720. If the applied control voltage is removed, electrochromic variable transmittance element 52 will self discharge, passing an increasing amount of light. Electrochromic variable transmittance element 52 may be rapidly cleared by shorting input node 720 to ground.

[0144] Resistor R17 connects input node 720 to the emitter of Darlington pair Q10 at node 722. The collector of Q10 is connected to a power supply through current limiting resistor R5, which may, for example, have an impedance of $27\ \Omega$. The base of Darlington pair Q10 is connected to digital output RB4 of microcontroller U1 through resistors R1 and R7. The base of Q10 is also connected to ground through resistor R4 and through resistor R7 and capacitor C16. Digital output pin RB4 is driven by pulse output 724 in response to pulse control 726 generated by software running on microcontroller U1. Pulse output 724 may produce a pulse signal such as, for example, a pulse width modulated signal. Preferably, pulse output 724 functions as a switch, setting output pin RB4 to either a high voltage or a low voltage once during each transition period as described below. Capacitor C16 and resistors R1, R4, and R7 form a low pass filter, shown generally by 728, to smooth the signal appearing on digital output RB4. This smoothing results in a substantially constant applied control voltage at input node 720 for a fixed desired control level. Additionally, the base-to-emitter diode drops in

Q10 together with the voltage divider formed between resistor R4 and the sum of resistors R1 and R7 set the operating voltage for electrochromic variable transmittance element 52. Typical values for components are 1 k Ω for R1 and R4, 100 Ω for R7, and 100 μF for C16. With digital output RB4 at 5 volts and nominal current draw by electrochromic variable transmittance element 52, input node 720 is approximately 1.2 volts.

[0145] The performance of control logic 66 can be improved through feedback of electrochromic variable transmittance element 52 applied control voltage at input node 720. Microcontroller U1 includes comparison logic to cause pulse output 724 to deliver a low voltage if the applied control voltage is greater than the desired control level and to deliver a high voltage otherwise. Typically, the high voltage is near V_{DD} and the low voltage is near ground. This comparison may be made by comparing a digital number representing the desired control level with the digitized applied control voltage obtained using an analog-to-digital converter (DAC). Alternately, DAC 730 and comparator 732 are used. DAC 730 produces a desired voltage level on analog output AN2 in response to the desired control level on DAC control 734 supplied by software running on microcontroller U1. Resistor R31 is connected between analog output AN2 and node 736 and resistor R26 is connected between node 736 and ground. One input of comparator 732, at analog input AN3, is connected to node 736. The other input of comparator 732, at analog input AN0, is connected to input node 720. The output of comparator 732 indicates if the desired voltage level is greater than the applied control voltage. Values for resistors R31 and R26 are chosen so that the voltage at node 736 is within the range of expected applied control voltages at input node 720 throughout the range of desired control voltages output from DAC 730. Typical values for R31 and R26 are 390 k Ω and 200 k Ω , respectively.

[0146] Positive feedback is achieved by connecting resistor R24 between node 736 and node 722. Resistor R17 is used to sense the drive current through electrochromic variable transmittance element 52 and, hence, is typically a low value such as $10\ \Omega$. Resistor R24 is typically a high value such as $1.3\ M\Omega$. As the drive current through resistor R17 increases, the voltage across resistor R17 increases pulling up the voltage at node 736. This increase in the voltage on the positive input terminal of comparator 732 has the regenerative effect of increasing the duty cycle from pulse output 724. This regenerative effect provides better system response at higher temperatures when electrochromic variable transmittance element 52 has an increased current draw together with an increase in maximum operating voltage. Positive feedback also offsets the effects of internal resistances within electrochromic variable transmittance element 52.

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[0147] Referring now to Fig. 37, a timing diagram illustrating electrochromic element transmittance control is shown. During automatic dimming operation, software executing in microcontroller U1 is initiated at transition points, one of which is indicated by 740, separated by fixed transition period 742. Desired control level 744 indicates the desired level of transmittance for electrochromic variable transmittance element 52. Desired control level 744 may be an analog value or, preferably, is a digital number determined by microcontroller U1. Desired control level 744 is compared to applied control voltage 746 by comparison logic. Comparator 732 accepts applied control voltage 746 and the desired control voltage appearing at node 736. Comparator output 738 produces difference signal 748, which is asserted when the desired voltage level representing desired control level 744 is greater than applied control voltage 746. Comparator output 738 is used to generate control signal 750 on output RB4. If desired control level 744 is greater than applied control voltage 746, digital output RB4 is

switched high. If desired control level 744 is less than applied control voltage 746, digital output RB4 is switched low. Preferably, low pass filter 728 filters control signal 750 to produce applied control voltage 746.

[0148] The duration of transition period 742 is set to inhibit flicker in electrochromic element 52 that may be noticed, for example, by vehicle operator 22. Transition period 742 may preferably be between two seconds and two microseconds. For the system described above, five milliseconds may be used for transition period 742.

[0149] Referring now to Fig. 38, a graph indicating dimmer reflectance as a function of applied control voltage is shown. Curve 754 plots percent reflectance for dimming element 50, containing electrochromic variable transmittance element 52, as a function of applied control voltage 756. Curve 754 indicates a decrease in reflection from about 86% to about 8% as the applied control voltage is increased from about 0.2 volts to about 0.9 volts. Fig. 38 also includes curve 756 illustrating current draw as a function of applied control voltage 756 for typical electrochromic variable transmittance element 52.

[0150] Referring again to Fig. 35, additional circuitry is provided to rapidly clear variably transmissive electrochromic element 50. Transistor Q11 is connected across variably transmissive electrochromic element 50 with collector at node 720 and emitter at ground. The base of transistor Q11 is connected through resistor R23 to digital output RB7. When digital output RB7 is asserted, transistor Q11 turns on, acting as a switch to rapidly discharge electrochromic variable transmittance element 52. Capacitor C6 is connected between the collector and base of transistor Q11 to reduce electromagnetic interference created as transistor Q11 switches. Transistor Q12 is connected between the base of transistor Q10 and ground and controlled by digital output RB7. Transistor Q11 turns on with transistor Q12 to shut off

transistor Q10 thereby preventing simultaneously attempting to darken and clear electrochromic variable transmittance element 52. Resistor R7 is placed between capacitor C16 and the collector of transistor Q12 to limit the discharge current from capacitor C16 through transistor Q12.

[0151] Referring now to Fig. 39, a flow diagram illustrating operation of control logic 66 for the rearview mirror 24, 26 is shown. As will be appreciated by one of ordinary skill in the art, the operations illustrated in Fig. 39 and other flow diagrams are not necessarily sequential operations. Also, though the operations are preferably implemented by software executing in microcontroller U1, operations may be performed by software, hardware, or a combination of both. The present invention transcends any particular implementation and aspects are shown in sequential flowchart form for ease of illustration.

[0152] An ambient light reading is taken and the average ambient light is initialized in block 760. When the automatic dimming system is initially powered up, the average ambient light level is initialized by taking a first reading of forward ambient light 32 using forward ambient light sensor 58. Acquiring an ambient light reading and the average ambient light level are described with regard to blocks 762 and 770, respectively, below.

[0153] An ambient light reading is taken and the log of the ambient light reading is found in block 762. The use of semiconductor forward ambient light sensor 58 with integral charge collection produces ambient light signal 60 having good resolution over a wide range of ambient light levels 32. As described above, this is accomplished by taking various readings of forward ambient light 32 using different integration periods 242, 248, 254 (Fig. 7). In one embodiment, four separate integration periods are used such as, for example, 600 μ s, 2.4 ms, 9.6 ms, and 38.4 ms. Each of these integration periods differs by a factor of four from

adjacent periods. Therefore, for example, the 2.4 ms integration period causes forward ambient light sensor 58 to be four times more sensitive to forward ambient light 32 than does integrating with the 600 μ s integration period. Typically, the shortest integration pulse 242 is first used by forward ambient light sensor 58 to produce short signal pulse 244. The width of short signal pulse 244 is measured by control logic 66. Since forward ambient light sensor 58 in complete darkness may still develop short signal pulse 244 having a width less than 100 μ s, a minimum threshold is set for accepting short signal pulse 244 as accurately reflecting the level of forward ambient light 32. Typically, this threshold may be 300 μ s. If short signal pulse 244 does not exceed the threshold, the next longest integration period is used by forward ambient light sensor 58. If the longest integration time does not yield a suitably long signal pulse, forward ambient light 32 is at an extremely low level and mirror 24, 26 can be operated at maximum sensitivity to glare 34.

[0154] Using the logarithm of ambient light signal 60 permits the use of an inexpensive microcontroller such as U1, which may have only 8-bit internal registers and no multiplication instructions. Since microcontrollers are binary devices, base two logarithms require fewer instructions to compute than base ten logarithms or natural logarithms. An algorithm is now described for obtaining an 8-bit binary logarithm having the most significant 4 bits representing an integer part and the least significant 4 bits a fractional part. The 8-bit ambient light signal 60 resulting from the proper integration period is examined bit by bit, starting with the most significant bit until the first binary one is found. The bit position containing the first binary one becomes the integer portion of the logarithm. The four most significant bits following the bit position containing the first binary one become the fractional portion of the logarithm. This value is incremented by one-sixteenth to better approximate the logarithm. An example of the

binary logarithm approximation is now provided. Suppose ambient light signal 60 is determined to be 44 (00101101 in base two). The most significant asserted bit is bit five, so the integer portion of the resultant value is binary 0101. The next four bits following bit five are 0110 so the fractional part of the resultant value is 0110 for a total value of 0101.0110. After incrementing, the binary logarithm approximation becomes 0101.0111.

[0155] Referring now to Fig. 40, a graph illustrating binary log approximation according to the above algorithm is shown. The binary logarithm is plotted for values of N between 1 and 255. Curve 790 shows the actual binary logarithm. Curve 792 shows the approximated binary logarithm.

[0156] Ambient light signal 60 must be scaled to compensate for different possible integration periods. This may be accomplished by adding a scaling factor to the binary logarithm of ambient light signal 60. For example, if the longest integration time (38.4 ms) is used to measure forward ambient light 32, a scale factor of 0 is added. If the next longest integration time (9.6 ms) is used, a scale of factor of 2 is added. If the next longest integration time (2.4 ms) is used, 4 is added. If the shortest integration time (600 μ s) is used, 6 is added. Since the largest value resulting from the binary logarithm approximation is 8 (1000.0000), no overflow results from adding the scale factor.

[0157] Referring again to Fig. 39, the logarithm of the ambient light level is compared to the day detect level in block 764. The day detect level is a calibrated value stored in microcontroller 66, read only memory, electronically erasable read-only memory, or the like, during manufacture. The day detect level is used to prevent dimming of, or to more rapidly clear dimming element 50, during rapid transitions from dark to bright such as if vehicle 20 emerges from a tunnel into daylight. If the logarithm of forward ambient light 32 exceeds a

preset day detect level, variable transmittance element 52 is cleared to set dimming element 50 to maximum reflectance in block 766. Processing is then delayed in block 768. A wait loop is entered having a time sufficiently long to make the period between taking ambient light readings equal a constant ambient light loop delay. This period may be, for example, 400 ms. Following the wait in block 768, another reading of forward ambient light 32 is taken in block 762. If the logarithm of forward ambient light 32 does not exceed the day detect level, an average is obtained in block 770.

[0158] The average of the logarithm of ambient light level is determined in block 770. Averaging readings first converted to the logarithm of forward ambient light 32 reduces the effect of a temporary bright light in front of vehicle 20 from dramatically skewing the average reading of an otherwise dark forward ambient light 32. A running average of the log of ambient light signals 50 may be obtained from a digital low pass filter such as is described by Equation 3:

$$y(n) = x(n)/64 + 63y(n-1)/64$$

[0159] where $x(n)$ is the most recently obtained binary log approximation of ambient light signal 60 correctly scaled for the integration period, $y(n-1)$ is the previous filter output, and $y(n)$ is the current filter output. The use of averaged logarithms with analog light signals is described in U.S. Patent No. 5,204,778 entitled "CONTROL SYSTEM FOR AUTOMOTIVE REARVIEW MIRRORS" issued to Jon H. Bechtel.

[0160] The average of the log of the ambient light level is compared to a threshold in block 772. The day detect level can be a calibrated value stored in microcontroller 66, read only memory, electronically erasable read-only memory, or the like, during manufacture. If forward ambient light 32 is sufficiently bright, vehicle operator 22 will not be dazzled by any

reasonable amount of glare 34, allowing mirror 24, 26 to be set to maximum reflectance. Therefore, if the average of the log of ambient light signal 60 is not less than the threshold, dimming element 50 is cleared in block 766 and the wait of block 768 is executed. If the average of the log of ambient light signals 50 is less than the threshold, glare processing occurs beginning in block 774. Typically, the threshold used for comparison in block 772 is less than the day detect level used in the comparison of block 764.

[0161] The glare integration period is determined in block 774. The integration period for glare sensor 62 is determined based on ambient light signal 60. The glare integration period is inversely proportional to the binary antilogarithm of the average of the log of ambient light signal 60 as described by Equation 4:

$$T_G(n) = \text{antilog}_2(K_1 - y(n)) - K_2$$

[0162] where $T_G(n)$ is the integration period for glare sensor 62 for the filter output at sample time n , K_1 is a multiplicative constant, and K_2 is an additive constant. Constants K_1 and K_2 are determined experimentally. If the average of the log of ambient light signal 60 is below a certain level, a maximum glare sensitivity integration period is used.

[0163] A glare count is set in block 776. The glare count indicates the number of glare readings taken between ambient light readings. The product of the glare count and the glare loop delay should equal the time between taking ambient light readings. For example, the glare count may be three and the time between taking glare readings may be 133 ms.

[0164] A glare reading is taken in block 778. The pulse width returning from glare sensor 62 as glare signal 64 is measured for the glare integration period determined in block 774. It is envisioned that a pre-measurement of the glare reading can optionally be made, prior to taking the measurement using the glare integration period determined in step 774, using a very short

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predetermined integration period similar to the integration period resulting from pulse 240 used for the forward light sensor, and may be an integration period as short as 30 to 40 μ s. If this short pre-measurement of glare is greater than a threshold level, the glare sensor is determined to be subject to a very high level of light indicating that the rear sensor is saturated. The mirror may be fully dimmed in response to this condition. If this pre-measurement does not exceed the threshold level, the processing will continue using the glare signal period determined in block 774.

[0165] The dimming element value is set in block 780. Glare signal 64 is used to determine desired control level 744 setting the reflectance for dimming element 50. This may be accomplished, for example, through the use of a look-up table which associates a lower reflectance with longer glare signal period. The precise relationship between the level of glare 34 and the setting for variable transmittance element 52 depends upon factors including the construction of mirror 24, 26, the configuration of vehicle 20, and preferential settings by operator 22. Desired control level 744 may be used to control variable transmittance element 52 as described above. For example, a manual actuated mechanism may be provided on the mirror to permit the user to adjust the relationship between the glare level and the transmittance of element 52.

[0166] A check of the glare count is made in block 782. If the glare count is zero, the next ambient light reading is taken in block 762. If the glare count is not zero, the glare count is decremented in block 784. A wait loop is then entered in block 786. The glare loop delay period is set so that glare readings are taken at regular, predetermined intervals.

[0167] A system for detecting moisture on window 100 (Fig. 41), shown generally by 102, includes light emitter 104 directed at window 100. Window 100 may be windshield 30, rear

window 36, or any other window on vehicle 20. Emitter 104 generates emitted radiation 106 that strikes window 100. A portion of emitted radiation 106 is reflected from window 100 as reflected radiation 108. The intensity of reflected radiation 108 is based on the amount of moisture on window 100.

[0168] Moisture light sensor 110 receives reflected radiation 108 and accumulates charge in response to light 108 incident over an integration period. Moisture light sensor 110 outputs light signal 112 based on the amount of light 108 incident on moisture light sensor 110 over the light integration period. The determination of the sensitivity for light sensor 110 may be generated within moisture light sensor 110 using the sensor logic of Fig. 17, or may be supplied by light sensitivity signal 114.

[0169] Ambient light 116 represents a source of noise that may mix with reflected radiation 108, affecting light signal 112. If window 100 is vehicle windshield 30, ambient light 116 may result from solar radiation, reflected sunlight, headlamps from oncoming vehicles, street lights, and the like, and may come from forward ambient light 32, skyward ambient light 46, or other light direction depending on the mounting and construction of sensor system 102. Ambient light 116 may vary over a wide dynamic range. Removing the effects of ambient light 116 improves the ability of moisture detecting system 102 to detect moisture. Various designs may be used to reduce the amount of ambient light 116 striking moisture light sensor 110 including channels and baffles for deflecting light away from moisture light sensor 110 and surfaces to reflect or refract ambient light 116 away from moisture light sensor 110 as is known in the art.

[0170] Control logic 66 is connected to light emitter 104 and moisture light sensor 110. Control logic 66 generates emitter signal 118 to turn on and off light emitter 104. In an embodiment, control logic 66 receives a first light signal 112 from moisture light sensor 110

with emitter 104 turned off to obtain an indication of the level of ambient light 116. Emitter 104 is then turned on. Control logic 66 receives a second light signal 112 from moisture light sensor 110. The presence of moisture on window 100 is then determined based on first and second light signals 112. If moisture is detected, control unit 66 may signal wiper control 120 to activate windshield wiper motor 112 to move wipers 38 over window 100. Control logic 66 may also signal defogger control 124 to activate defogger 40. Control logic 66 may also signal defroster control 126 to activate defroster 42. Other means for removing moisture from window 100 may also be used within the spirit and scope of the present invention.

[0171] In the embodiment shown in Fig. 41, a single light emitter 104 and a single moisture light sensor 110 are shown. However, it is within the spirit and scope of the present invention to include more than one emitter 104, more than one moisture light sensor 110, or a plurality of both emitters 104 and sensors 110. Also, control logic 66 may be adapted to control a wide variety of functions including closing windows, cleaning windows, activating lamps, and the like.

[0172] Referring now to Fig. 42, a ray diagram illustrating moisture detection on an outside surface causing an increase in reflected light is shown. Window 100 has outer surface 130 and inner surface 132. In the absence of moisture, emitted radiation 106 passes through inner surface 132 and outer surface 130 to become exiting ray 134. Moisture on outer surface 130, such as droplet 136, causes at least some of emitted radiation 106 to be reflected as reflected radiation 108, which is detected by moisture light sensor 110 and converted to discrete light signal 112. A second light sensor, indicated by 110a, may be positioned to detect moisture on inner surface 132. Emitted radiation 106 may reflect off moisture, such as fog or frost, on

inner surface 132 producing reflected radiation 108a. Second moisture light sensor 110a generates discrete light signal 112a indicating the presence of moisture on inner surface 132.

[0173] Referring now to Fig. 43, a ray diagram illustrating moisture detection on an outside surface causing a decrease in reflected light is shown. Light emitter 104 is positioned such that emitted radiation 106 strikes inner surface 132 at an angle of incidence α allowing emitted radiation 106 to pass through inner surface 132 and be totally reflected between outer surface 130 and inner surface 132 at least once before exiting as reflected radiation 108. To facilitate emitted radiation 106 entering inner surface 132, emitter 104 is placed in input coupler 140, which is attached to inner surface 132. To facilitate reflected radiation 108 exiting inner surface 132, moisture light sensor 110 is placed in output coupler 142, which is attached to inner surface 132. Input coupler 140 and output coupler 142 are constructed of a material having an index of refraction similar to the index of refraction of window 100. For window 100 constructed of glass and surrounded by air, the index of refraction is approximately 1.49 and the angle of incidence α must be greater than 42°. If moisture, such as droplet 136, is present on outer surface 130 or inner surface 132, total reflection between outer surface 130 and inner surface 132 is impaired, permitting exiting ray 144. This decreases reflected radiation 108 received by moisture light sensor 110. Moisture light sensor 110 outputs discrete light signal 112 indicating the intensity of reflected radiation 108.

[0174] Input coupler 140 and output coupler 142 may be designed to reduce the effect of ambient light 116 reaching moisture light sensor 110. In particular, reflective and refractive surfaces on coupler 140, 142 serve to direct reflected radiation 108 into moisture light sensor 110 and direct ambient light 116 away from moisture light sensor 110. Flanges, baffles, shields, and the like may also block ambient light 116. Couplers may further be designed to

prevent spurious reflected radiation from layers within window 100. Various designs for couplers 140, 142 are well known in the art.

[0175] The designs represented by Figs. 42 and 43 may be combined in a single device to provide greater sensitivity to moisture and to permit detecting moisture on both outer surface 130 and inner surface 132. For use in detecting moisture on windshield 30, light emitter 104 and moisture light sensor 110 are preferably mounted to monitor moisture in a region of windshield 30 wiped by windshield wipers 38. Mounting locations include within or beside the interior rearview mirror mounting foot or just above the dashboard.

[0176] Referring now to Fig. 44, a flow diagram illustrating operation of control logic for automatically removing moisture from a vehicle window is shown. Operations may be executed using control logic 66 as described above or similar circuitry. The present invention transcends any particular implementation and aspects are shown in sequential flow chart form for ease of illustration.

[0177] Moisture light sensor 110a is read with light emitter 104 switched off to obtain a level of ambient light 116 in block 800. Emitter 104 is activated and light sensor 110a is read a second time to determine the amount of reflected radiation 108a from interior surface 132 in block 802. In an embodiment, the integration period for the second reading is based on the level of ambient light obtained in block 800, such that the brighter the previous ambient light measurement, the shorter the integration period used in the current measurement. In another embodiment, the intensity of emitted radiation 106 from emitter 104 is modified based on the level of light determined in block 800. The level of intensity of emitted radiation 106 may be controlled by using a pulse width modulated voltage for emitter signal 118.

[0178] Light signal 112a produced with emitter 104 turned on is compared to light signal 112a produced with emitter 104 turned off in block 804. If the difference between light signal 112a produced with emitter 104 on and light signal 112a produced with emitter 104 off exceeds an interior surface threshold, one or more means for removing moisture from interior window surface 132 are turned on in block 806. If the difference is not greater than the interior surface threshold, a check is made to determine if means for removing moisture from exterior window surface 130 should be activated beginning with block 808.

[0179] In an embodiment of the present invention, the interior surface threshold, which can be a calibrated value, is based on the level of ambient light 116 obtained in block 800. In another embodiment, two thresholds are used. In addition to the interior surface threshold, a second, greater threshold is used to determine if a check should be made after activating the means for removing moisture 38 from exterior window surface 130. If reflected radiation 108a is too great, excessive moisture is present on inside surface 132, and an accurate reading of the moisture on outer surface 130 cannot be obtained. If the level of reflected radiation 108a is between the two thresholds, the means for removing moisture from interior window surface 132 is activated and then a check is made whether to activate means for removing moisture from exterior window surface 130.

[0180] Moisture light sensor 110 is read with light emitter 104 switched off to obtain a level of ambient light 116 in block 808. Emitter 104 is activated and light sensor 110 is read a second time to determine the amount of reflected radiation 108 from exterior surface 130 in block 810. In an embodiment, the integration period for the second reading is based on the level of ambient light obtained in block 808. In another embodiment, the intensity of emitted radiation

106 from emitter 104 is modified based on the level of ambient light 116 obtained in block 808 and on the level of reflected light 108a detected by light sensor 110a.

[0181] Light signal 112 produced with emitter 104 on is compared to light signal 112 produced with emitter 104 off in block 812. In a preferred embodiment, the configuration of emitter 104 and light sensor 110 described above is used. Hence, if the difference between light signal 112 produced with emitter 104 on and light signal 112 produced with emitter 104 off is less than an exterior surface threshold, means for removing moisture from exterior window surface 130 are turned on in block 814. The check for activating means for removing moisture from interior window surface 132 beginning with block 800 is then repeated.

[0182] In an embodiment, the comparison of block 812 includes the level of reflected radiation 108a off inner surface 132. This is because reflected radiation 108 can be no greater than emitted radiation 106 less reflected radiation 108a. In another embodiment, the exterior threshold is based on the level of ambient light 116 obtained in block 808.

[0183] Many other algorithms for determining the presence of moisture on a window of vehicle 20 may be used within the spirit and scope of the present invention. Some of these algorithms are described in U.S. Patent Nos. 5,796,106 to Noack; 5,386,111 to Zimmerman; 5,276,389 to Levers; 4,956,591 to Schierbeek et al.; 4,916,374 to Schierbeek et al.; 4,867,561 to Fujii et al.; 4,859,867 to Larson et al.; 4,798,956 to Hochstein; 4,355,271 to Noack; and RE 35,762 to Zimmerman.

[0184] A moisture detection system may use emitter 104 having a principal emission band across any of the visible or invisible light spectrum. Moisture light detector 110 must be constructed based on the desired spectrum emitted by emitter 104. A preferred spectrum is weighted to the infrared range. Consequently, no filtration may be required for moisture light

detector 110, 110a. Alternatively, a filter that limits non-infrared light may be used for the moisture detector.

[0185] Referring now to Fig. 45, a system for controlling headlamps is shown. Skyward ambient light sensor 150 is mounted to view light illuminating the view seen by operator 22. Preferably, skyward ambient light sensor 150 is positioned to receive skyward ambient light 46 from an area generally above and in front of vehicle 20. Skyward ambient light sensor 150 generates skyward ambient light signal 152 based on the amount of light incident on skyward ambient light sensor over an integration period. The integration period may be advantageously varied according to the control signal of Fig. 7. Control logic 66 uses skyward ambient light signal 152 to activate headlamp control circuitry 154 activating one or more headlamps 44. Preferably, ambient light filter 156 filters skyward ambient light 46 reaching skyward ambient light sensor 150 to attenuate infrared components of skyward ambient light 46. The filter characteristics of the ambient light filter 156 are shown in Fig. 48. As can be seen from Fig. 48, the filter has a peak response at approximately 475 nm. Such a filter will be highly sensitive, capable of detecting light under both cloudless and cloudy conditions. Alternatively, the filter may be selected to provide the light sensor 150 with a spectral response similar to photopic response curve 610. The filter should at least attenuate infrared light to be input to sensor 150.

[0186] An advantageous embodiment permits compensating for weather conditions in determining the state for headlamps 44. This is accomplished using a second skyward ambient light sensor 158 with ambient light filter 160 generating skyward ambient light signal 162 for control logic 66 is included. In this embodiment, the ambient light filters 156, 160 attenuate different portions of skyward ambient light 46. As examples, one filter may be cyan and the

other red or one may be blue and the other near infrared. Since the spectral composition of skyward ambient light 46 is different on clear days than on cloudy days, the ratio of the incident light represented by ambient light signals 152 and 162 will give an indication of the type of day. Thresholds for determining the state of headlamps 44 can then be varied based on the determined ratio.

[0187] Referring now to Fig. 46, a graph illustrating the differences in the spectral content of ambient light on a cloudy day and ambient light on a clear day is shown. The spectral characteristics of skyward ambient light 46 vary depending on weather conditions. A typical cloudless day may have a spectrum, normalized to a relative intensity of 1.0 at 620 nm, as shown by curve 820. A typical cloudy day may have a spectrum, normalized to a relative intensity of 1.0 at 620 nm, as shown by curve 822. Comparing curves 820 and 822 shows that clear days have a significantly bluish spectrum as compared to cloudy days. Since vehicle operator 22 perceives dim ambient light 46 from a cloudless sky as being brighter than ambient light 46 of a similar intensity from a cloudy sky, this difference in spectral composition may be used to modify the one or more thresholds used to control vehicle headlamps 44.

[0188] Referring now to Fig. 47, a flow diagram illustrating operation of control logic for automatically controlling vehicle headlamps is shown. Operations may be executed using control logic 66 as described above or similar circuitry. The present invention transcends any particular implementation and aspects are shown in sequential flowchart form for ease of illustration.

[0189] Skyward ambient light 46 is read using skyward ambient light sensor 150 in block 830. Skyward ambient light 46 is read using skyward ambient light sensor 158 in block 832. Light sensors 150, 158 filter ambient light 46 through filters 156, 160 respectively. The spectral

characteristics of filters 156, 160 are chosen so that ambient light 46 detected by light sensor 150 is bluer than ambient light 46 detected by light sensor 158. This may be accomplished, for example, by using cyan filter 156 and red filter 160, blue filter 156 and infrared filter 160, or the like. Filters 156, 160 may be incorporated into light sensors 150, 158 or may be separate elements as described above.

[0190] The relative cloudiness is estimated in block 834. In particular, the ratio of the outputs from light sensors 150, 158 may be obtained to indicate the relative blue content of ambient light 46. This ratio is used to determine one or more thresholds in block 836. Each threshold is used as a basis of comparison to determine control of headlamps 44. It is envisioned that the value may be calibrated. Calibration as used in this application can refer to a sensor or a threshold being calibrated using a coefficient value stored in microcontroller 66, read only memory, electronically erasable read-only memory, or the like, during manufacture, which coefficient value can represent the ratio of a standard value to an actual measurement for a subject sensor exposed to known light levels measured in a tester prior to, or after, being installed in a circuit. It is envisioned that the control logic 66 will obtain thresholds from a look-up table, although they may be calculated using a formula, or a combination of a look-up table and a formula.

[0191] The level of ambient light 46 is compared against a day threshold in block 838. If the intensity of ambient light 46 is greater than the day threshold, headlamps 44 are set to daylight mode. This may be turning headlamps 44 off or setting headlamps 44 on at a daylight running intensity. The output of either of light sensors 150, 158 may be used in the comparison. In an alternative embodiment, a daylight threshold is calculated for each light sensor 150, 158, with daylight running mode set if the intensity measured by either sensor 150, 158 exceeds its

threshold. In another embodiment, daylight running mode is set if the output from both sensors 150, 158 exceeds their respective thresholds.

[0192] If the level of ambient light 46 is less than the day threshold, a comparison is made with the night threshold in block 842. If the level of ambient light 46 is greater than the night threshold, headlamps 44 are set to low beam mode in block 844. If not, headlamps 44 are set to high beam mode in block 846. While the headlamp control system described by Fig. 44 shows three states for headlamps 44, one of ordinary skill in the art will recognize that the present invention may be used in other systems, including dual state headlamps 44 and continuously variable headlamps 44.

[0193] It is further envisioned that a skyward sensor 150 and/or 158 can be used in combination with forward sensor 58 to detect a condition under which the headlights should be turned on without delay, for example, when vehicle 20 enters a tunnel. It is desirable for the headlights to turn ON immediately upon the sky sensor detecting a night condition, as opposed to subjecting the change to a delay, when entering a tunnel. A tunnel can be detected using a sky ambient light sensor looking through a lens with a narrow focus and the forward sensor looking through a lens with a broad focus. For such an embodiment, ambient light filter 156 (Fig. 7) can comprise a lens providing a narrow focus for sky sensor 150 and filter 68 can comprise a lens providing a wide field of view for sensor 58. It is envisioned that the lenses could be incorporated into the encapsulant shapes of the sensor or provided by discrete lenses positioned in front of the sensors to control the field of view for the sensors. When the forward sensor 58 detects a darker image than the sky sensor 150, the control unit may anticipate a tunnel. Under such conditions, as soon as the sky sensor detects night conditions, the headlights will turn ON with no delay or a very short delay, such as a delay of 1-2

seconds. Under other conditions, such as where the forward sensor 58 detects light, it is may be desirable for the system to delay for 10-30 seconds turning the headlights ON so that the headlights do not flash ON and OFF.

[0194] In particular, in one embodiment, a high threshold and a low threshold are used for the sky sensor. The forward ambient light sensor 58 can be used for selecting the timing adjustments such that the delay for changing the headlight state is dependent upon the forward measurement through light sensor 58. The short delay for transitioning from OFF to ON can be 1 second, such that if the sky sensor 150 measurement drops below the low threshold for more than 1 second, the headlights will turn ON. The long delay for transitioning the headlight from OFF to ON can be 15 seconds, such that if the sky sensor 150 measurement drops below the low threshold for more than 15 seconds, the headlights will turn ON. The short delay for transitioning from ON to OFF can be 5 seconds, such that if the sky sensor 150 measurement is above the high threshold for more than 5 seconds, the headlights will turn OFF. The long delay for transitioning from ON to OFF can be 15 seconds, such that if the sky sensor 150 measurement remains above the high threshold for more than 15 seconds, the headlights will turn OFF. The ON short period will be initiated when the forward sensor 58 detects darkness while the sky ambient sensor detects light conditions and the lights are OFF. The OFF short period will be initiated when the forward sensor 58 detects daylight conditions while the sky sensor detects night conditions and the lights are ON. The long delays can be used for other conditions. Headlights ON refers to nighttime lights (*e.g.*, high or low beams) and headlights OFF refers to daylight lights (*e.g.*, no headlights or daylight running lights). The low threshold can correspond to 1300 to 1500 lux seen by the sky sensor. The high threshold can correspond to 1800 to 2100 lux seen by the sky sensor. The ratio of the high to

low thresholds can be 1.3 to 1.5. It is further envisioned that if either the forward sensor 58 or sky sensor 150 detects a light level below a very low level, such as 40 to 100 lux, the headlamps will switch on without significant delay regardless of any other sensed conditions. It is also envisioned that the time periods described herein can be proportional to the vehicle's speed, such that the faster the vehicle is traveling, the shorter will be the delays.

[0195] As illustrated in Fig. 2, the mirror can include skyward sensors 150, 158 on one end of mirror 24 and skyward sensors 150', 158' on the other end of the mirror. It will be recognized that cars are manufactured for drivers on either the right side or left side of the vehicle depending upon the country where the vehicle will be sold. The optional provision of two sets of sensors will result in one set being positioned on the end of the mirror closest to the window regardless of whether the mirror is installed in a vehicle having right side or left side driver operation. In operation, the control logic 66 will monitor the outputs from sensors 150, 150', 158, 158' to determine which of the light sensors is collecting more light in high ambient light conditions while the vehicle is traveling at a relatively high speed. The side of the mirror containing the sensors with the highest light output will be used for the ambient sky sensors. The other light sensor outputs will not be used, as the vehicle roof will shade them. In this manner, the vehicle can automatically detect whether the mirror is angled for a driver on the right or left side of the vehicle.

[0196] The use of cylindrical light sensor 170' to implement the glare sensor 62 orientated with the longitudinal axis horizontal provides significant advantages for the automatic control of the electrochromic mirror. The lens radius r (Fig. 26a) for this sensor can, for example, be 1.25 mm, producing a focal distance f of 2.5mm, and the distance d between the exposed surface of the light transducer and the tip of the light sensor encapsulant can be 2.15 mm. The

glare sensor 62 encapsulant can be transparent, having no diffusant therein. In particular, with the glare sensor positioned in the rearview mirror housing such that the longitudinal axis of the cylindrical lens is oriented horizontally, a wide horizontal viewing angle is achieved.

[0197] Of particular advantage is the off-axis light sensitivity distribution of the lens 170', which is shown in Fig. 49. In Fig. 49, the center axis corresponds to the center of the transducer region 532. As can be seen, the cylindrical lens has high off-axis sensitivity along its longitudinal axis. This is better illustrated in the rectangular view of the sensitivity curve shown in Fig. 50. The peak off-axis sensitivity occurs at an angle of approximately 50°. This characteristic can be used to improve detection of light from a passing vehicle, which is of particular interest when the inside mirror controls the outside mirror. In particular, a passing vehicle's headlights will be off axis from the glare sensor located in the interior rearview mirror 24 even though it is shinning on the exterior rearview mirror 26. A conventional glare sensor located on the interior mirror will detect diminished light from the passing vehicle, and thus increase the mirror reflectance, when the lights from the passing vehicle no longer shines directly through the rear window. The improved glare sensor 170' has increased sensitivity to off-axis light, and thus will be increasingly sensitive to lights within viewing angle β . Thus, the reduced reflectivity of mirror 26 will be maintained until the passing vehicle headlights are no longer visible to operator 22 through mirror 26. Those skilled in the art will recognize that the off-axis distribution of the light sensors can be significantly reduced by adding a diffusant or diffusing projections to the encapsulant, which is preferably done if the cylindrical lens sensor is used to implement ambient sensors 58, 150, 150', 158, 158'.

[0198] In addition to separately controlling headlamps 44, automatic dimming of mirrors 24, 26, and various means for removing moisture from windows such as wipers 38, defogger 40,

defroster 42, and the like, benefit may be achieved by combining light sensors 170 and control logic 66 from different applications. For example, control logic 66 can control the state of headlamps 44 based on the level of light detected by at least one sky ambient light sensor 150, 158. Control logic 66 may also control dimming of at least one rearview mirror 24, 26 based on levels of light detected by forward ambient light sensor 58 and glare light sensor 62. Control logic 66 may then also turn ON headlamps 44 when the level of light detected by forward ambient light sensor 58 is below a threshold level. This would turn ON headlamps 44 in situations such as tunnels or extended overpasses when overhead lighting may provide sufficient light detected by sky ambient light sensor 150, 158 to turn headlamps 44 off, but the area in front of vehicle 20 is relatively dimly lit.

[0199] In another example, control logic 66 determines the amount of moisture on a cleared area of a window of vehicle 20, such as windshield 30 or rear window 36, based on the output from at least one moisture sensor 102. Control logic 66 controls means for removing moisture 38, 40, 42 based on the determined amount of moisture. Control logic 66 further controls the dimming of rearview mirror 24, 26 based on the amount of moisture and the levels of light detected by forward ambient light sensor 58 and glare light sensor 62. This would permit control logic 66 to undim mirror 24, 26 if a window through which light was received by forward ambient light sensor 58 or glare light sensor 62 was covered by moisture such as frost, snow, fog, and the like. Also, for a window cleaned by wipers 38, readings from forward ambient light sensor 58 or glare light sensor 62 may be ignored during intervals when one of the wipers 38 passes in front of light sensor 58, 62.

[0200] In still another example where control logic 66 determines the amount of moisture on a cleared area of a window of vehicle 20 and controls means for removing moisture 38, 40, 42,

the control of headlamps 44 may be based on detected moisture as well as the level of light detected by one or more sky ambient light sensors 150, 158. Again, this would permit control logic 66 to set headlamps 44 to a predetermined state if a window through which light was received by forward skyward light sensor 150, 158 was covered by moisture. Also, for a window cleaned by wipers 38, readings from skyward ambient light sensor 150, 158 may be ignored during intervals when one of the wipers 38 passes in front of light sensor 150, 158.

[0201] The present invention may be readily adapted to control other equipment on vehicle 20 besides or in addition to headlamps 44, automatic dimming of mirrors 24, 26, and various means for removing moisture from windows 38, 40, 42. For example, electrically powered windows, sunroofs, moon roofs, convertible tops, and the like may be automatically closed when moisture such as rain is detected. Also, various lighting in addition to headlamps 44, such as running lights, park lights, puddle lights, courtesy lights, dashboard lights, and the like may be automatically controlled based on one or more of ambient lighting conditions, the detection of moisture, the running state of vehicle 20, and the like. The state of passenger compartment heating and cooling systems, including air conditioning, heater, vent positions, windows, and the like may be automatically controlled based on one or more of ambient lighting conditions, the detection of moisture, the running state of vehicle 20, internal temperature, external temperature, and the like.

[0202] Control logic 66 for receiving light signals 164 from multiple light sensors 170 and generating control signals 166 for equipment of vehicle 20 may be in one housing or may be distributed throughout vehicle 20. Elements of control logic 66 may even be included within light sensors 170. Elements of control logic 66 may be interconnected through a variety of means including discrete wiring, buses, optical fiber, radio, infrared, and the like. Control

logic 66 may comprise many cooperating processors or a single multitasking processor. Operations may be implemented in software, firmware, custom hardware, discrete logic, or any combination. The present invention does not depend on the method or means of implementing control logic 66.

[0203] It is envisioned that outside fog of the type requiring activation of front and/or rear fog lights could be automatically detected using a reflected light detection system substantially similar to that provided for the moisture detector. To detect such outside fog, a light source and sensor are spaced by a distance such that light from the emitter that will be detected by the sensor is reflected from a point several meters from the vehicle. Under circumstances where the detected reflected light level is substantially constant, greater than a threshold level, and continuously detected over a substantial period of time, front and/or rear vehicle fog lamps can be turned on automatically.

[0204] Thus, it can be seen that an improved equipment control system is disclosed. The system is easier to manufacture since variations in the performance of the light sensors can be compensated for in the microcontroller. The mirror is readily manufacturable by automated means. Additionally, the system can be provided at a lower cost as low cost control logic can be utilized. The system reliably detects light over a wide light range and with significantly reduced temperature dependence.

[0205] While embodiments of the invention have been illustrated and described, it is not intended that these embodiments illustrate and describe all possible forms of the invention. Rather, it is intended that the following claims cover all modifications and alternative designs, and all equivalents that fall within the spirit and scope of this invention.